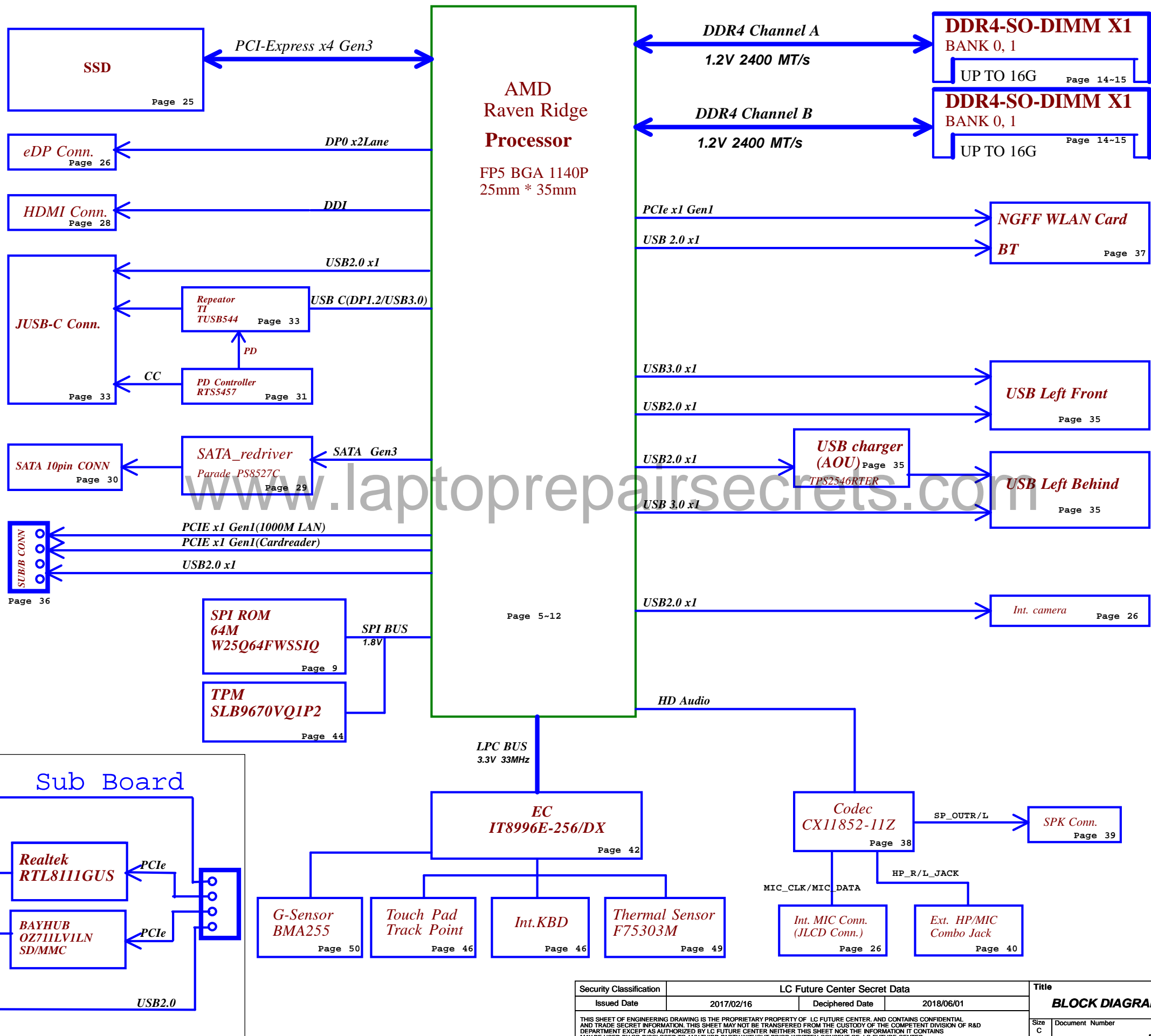


LCFC Confidential
RAVEN EX85 Rev1.0 Schematic

AMD Raven Ridge FP5 Processor with DDR4

2018-04-10 Rev1.0

AMD Raven Ridge FP5



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Title		BLOCK DIAGRAM	
Size	C	Document Number	JINN/DOOKU
Date:	Tuesday, April 10, 2018	Sheet	2 of 65
Rev	1.0		

Voltage Rails (O --> Means ON , X --> Means OFF)

Power Plane \ State	B9+	+3VALW +5VALW +1.8VALW +0.9VALW_VDDP	+1.2V +2.5V	+5VS +3VS +1.5VS +0.9VS_VDDP +0.6VS +VDDCR_SOC +VDDC_VDD +VGA_CORE +3.3VGS +1.8VGS +1.35VGS +0.95VGS
S0	O	O	O	O
S3	O	O	O	X
S5 S4/AC Only	O	O	X	X
S5 S4 Battery only	O	X	X	X
S5 S4 AC & Battery don't exist	X	X	X	X

STATE \ SIGNAL	SLP_A#	SLP_S3#		SLP_S5#	EC_ON	SUSP#
S0	HIGH	HIGH		HIGH	ON	ON
S3 (Suspend to RAM)	LOW	LOW		HIGH	ON	OFF
S4 (Suspend to Disk)	LOW	LOW		HIGH	ON	OFF
S5 (Soft OFF)	LOW	LOW		LOW	ON	OFF

USB2 Port

Port	Device
0	USBC
1	USB3 port1
2	USB3 port2
3	USB2 port1
4	USB2(BT)
5	Int. Camera

USB3 Port

Port	Device
0	USB Type-C
1	USB3 port1
2	USB3 port2
3	USB3 port2
	X
4	X

PCIE Port

Port	Device
GPP0	LAN
GPP1	CardReader
GPP2	X
GPP3	X
GPP4	WLAN
GPP5	X
GFX0	M.2 SSD
GFX1	M.2 SSD
GFX2	M.2 SSD
GFX3	M.2 SSD
GFX4~7	X

SATA Port

Port	Device
1	HDD
2	X

SMBUS Control Table


SOURCE	Main VGA	BATT	SODIMM	WLAN WiMAX	Thermal Sensor	APU	CP Module	Charge	PD	G-Sensor	PMIC
EC_SMB_CK1 EC_SMB_DA1	IT8996E-256-DX +3VL	X	V +3VL	X	X	X	X	V +3VL	X	X	X
EC_SMB_CK2 EC_SMB_DA2	IT8996E-256-DX +3VALW	X	X	X	X	X	X	X	V +3VALW	X	X
EC_SMB_CK3 EC_SMB_DA3	IT8996E-256-DX +3VS	V +3VS_VGA	X	X	X	V +3VS	V +3VS	X	X	V +3VS	X
EC_SMB_CK4 EC_SMB_DA4	IT8996E-256-DX +3VL	X	X	X	X	X	X	X	X	X	V

APU I2C

APU Port	Net	Device
I2C2_SCL/SDA	APU_SMB0CLK/SDA	DIMM1/DIMM2
I2C3_SCL/SDA	APU_SMB1_CLK/SDA	TOUCHPAD
SIC/SID	EC_SMB_CK3/DA3	Conn EC
USBC_I2C_SCL/SDA	EC_SMB_CK2/DA2	PD

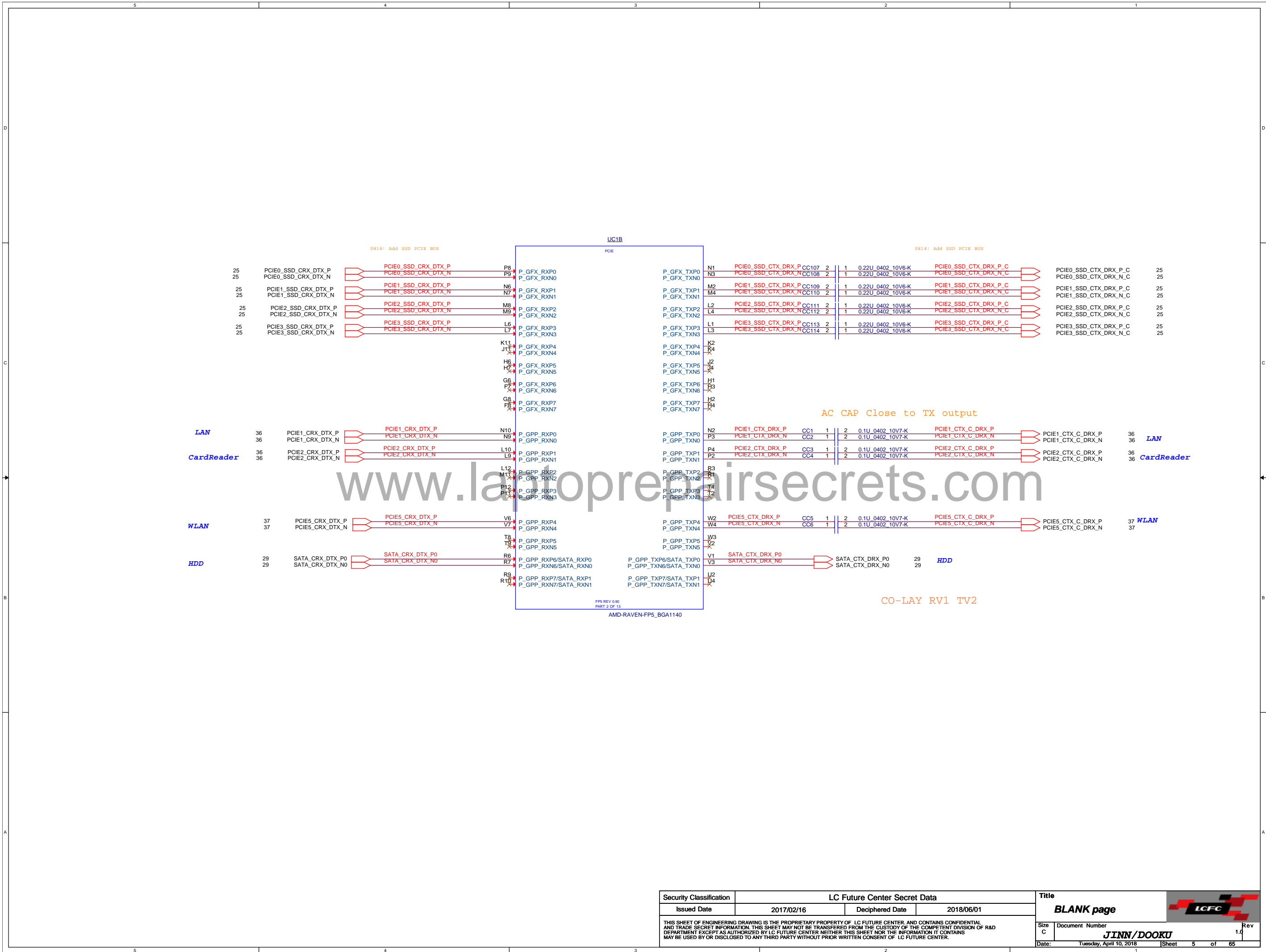


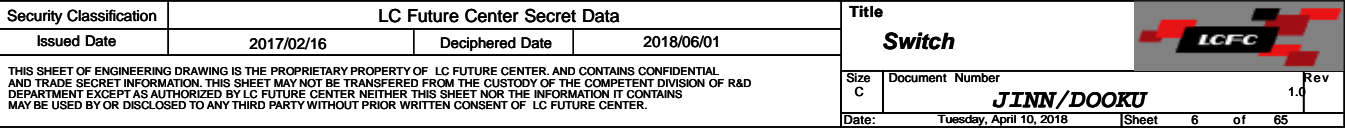
APPLY PCB PN

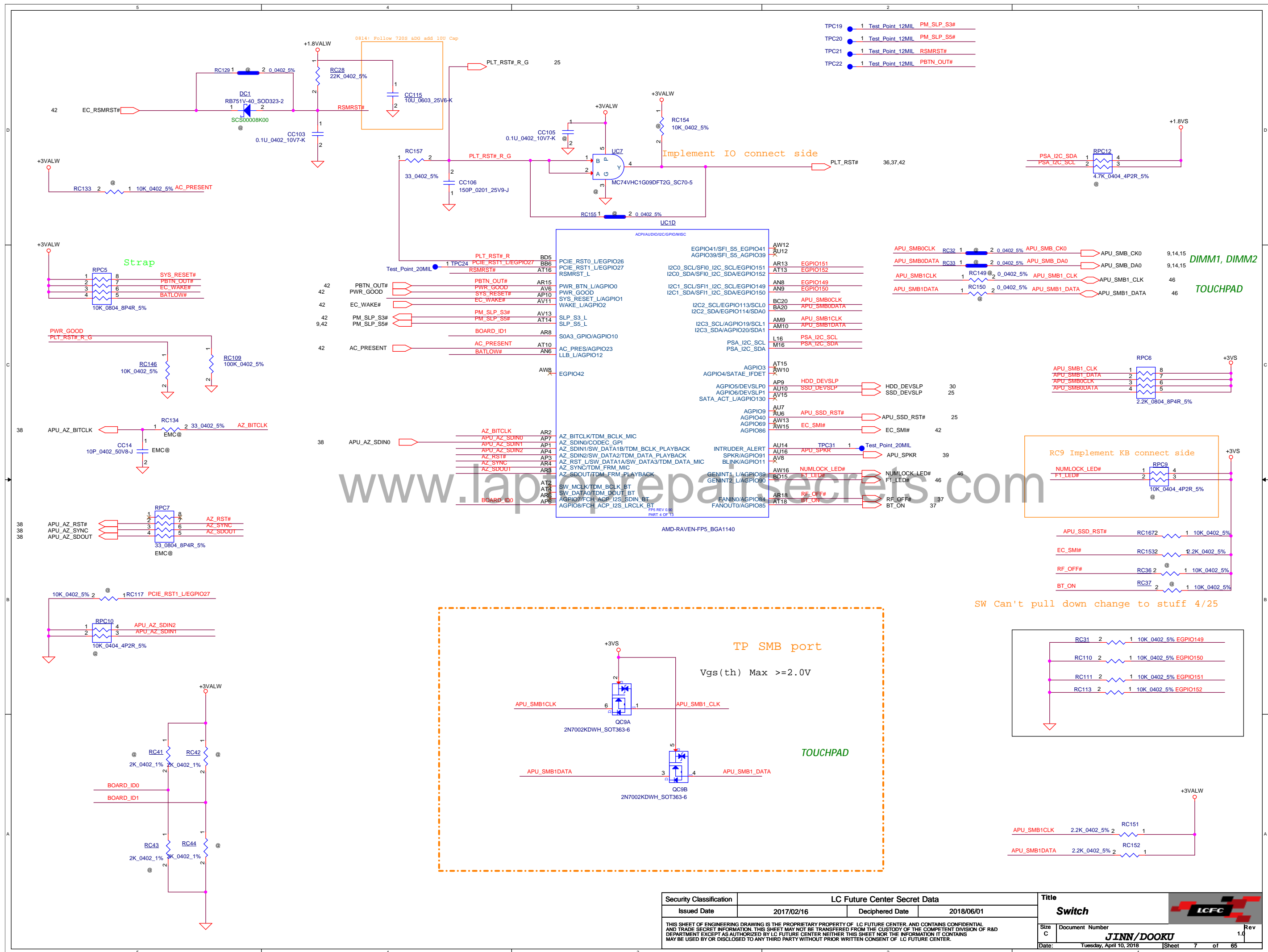
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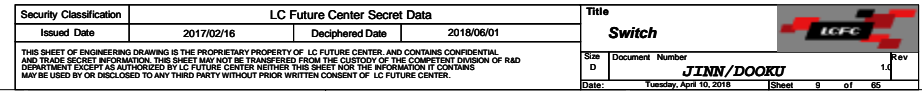
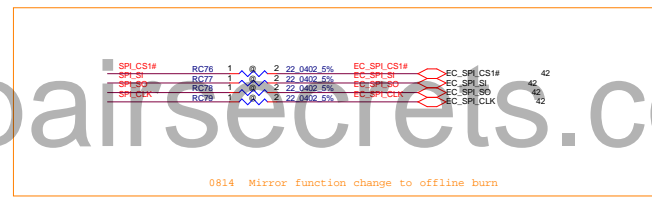
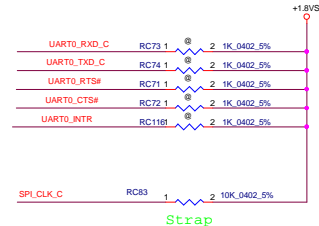
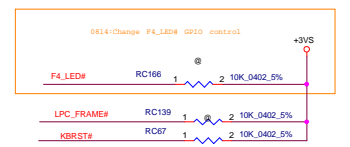
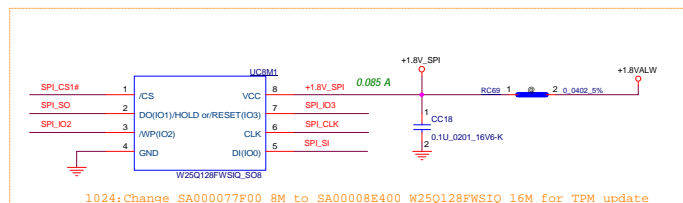
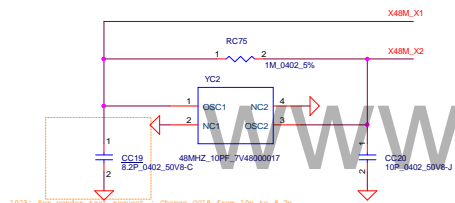
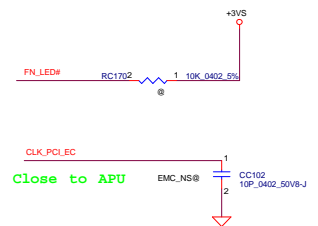
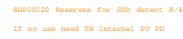
BOM Structure Table

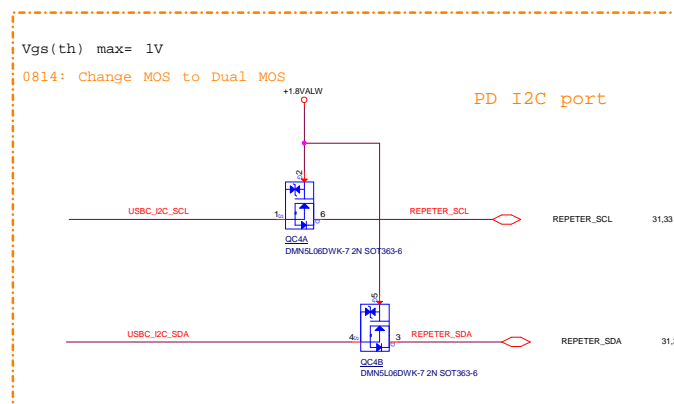
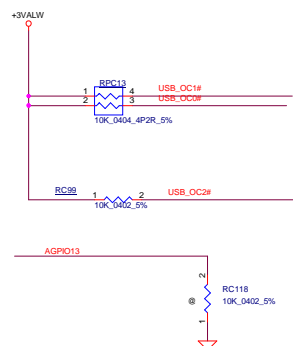
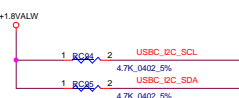
BOM Structure	NOTE
HDT@	For HDT AMD debug port
LPC@	For LPC AMD debug port
TPM@	Trusted Platform Module(TPM)
UMA@	UMA SKU ID
CD@	COST DOWN
EMC_NS@	EMC Reserves
ME@	ME Connector
RF@	For RF function
EMC@	For EMI function
RF_NS@	reserves RF component
RF_PXNS@	VGA reserves RF component






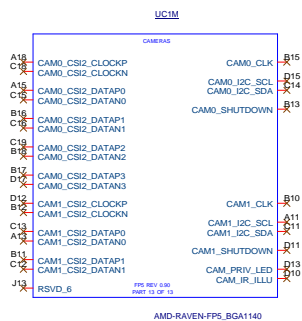




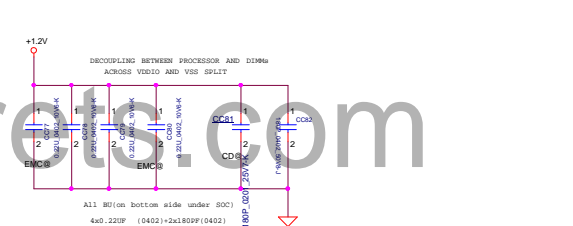
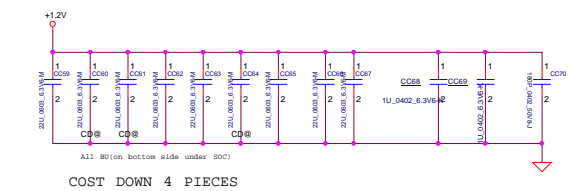
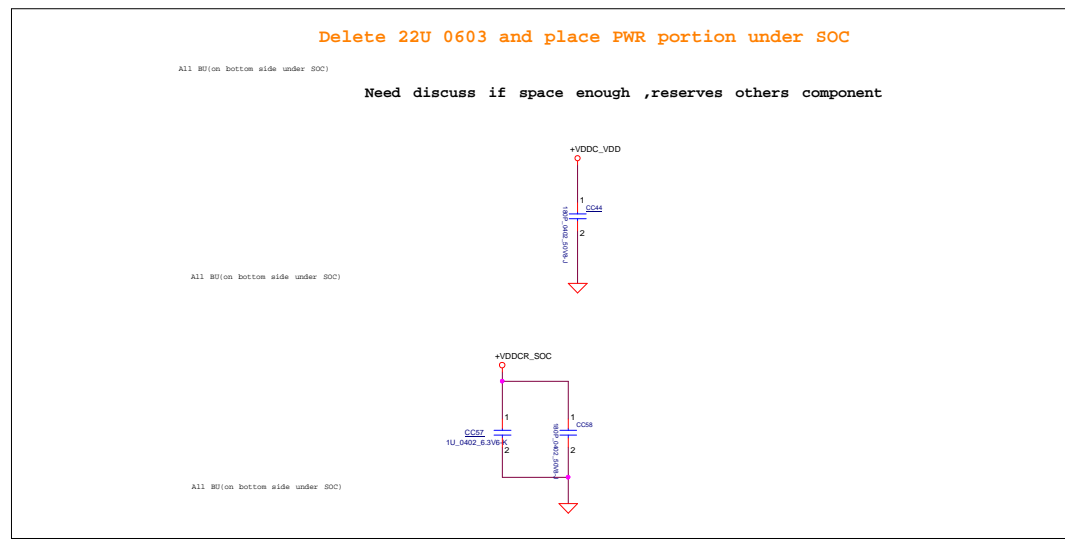
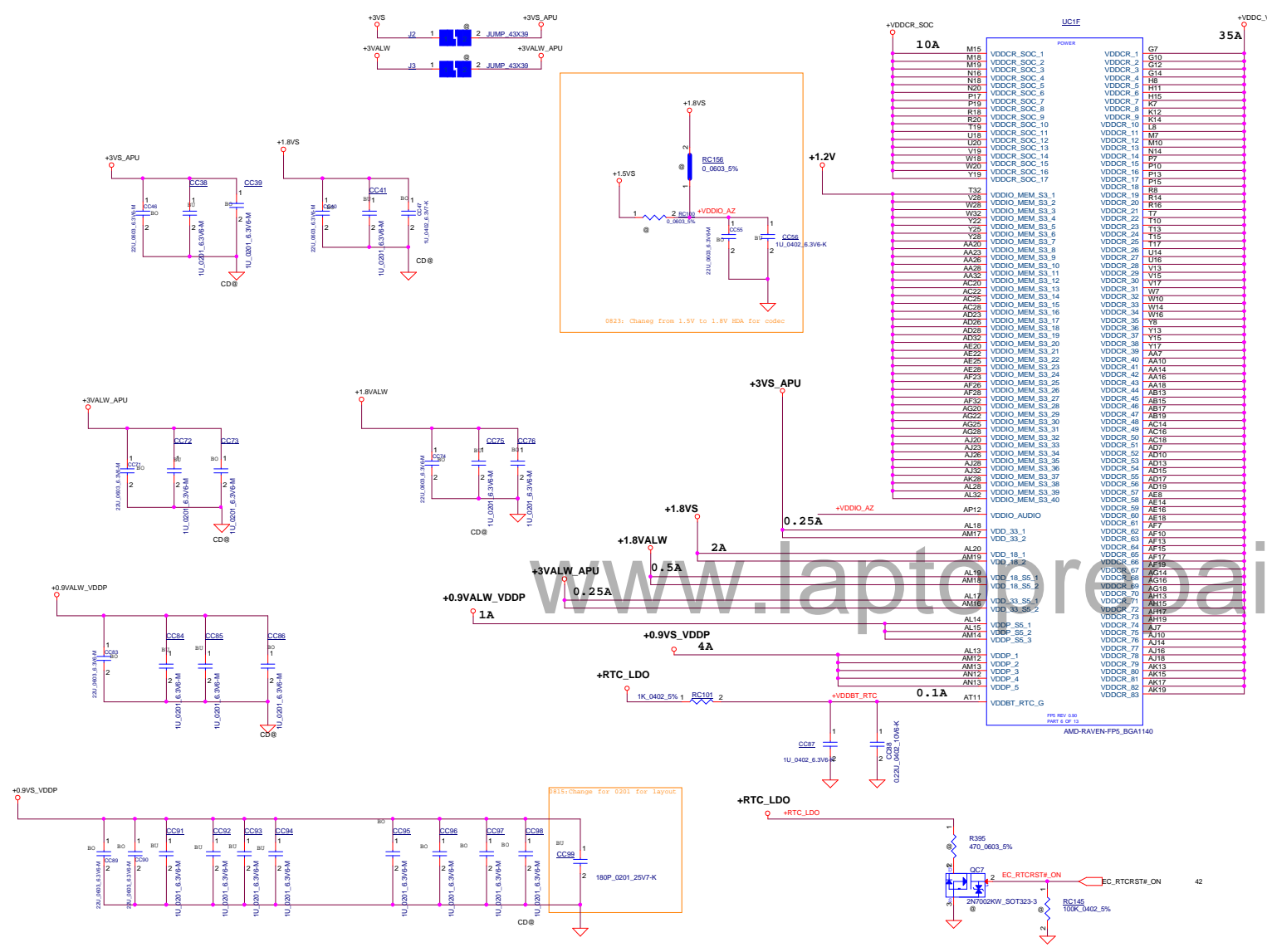


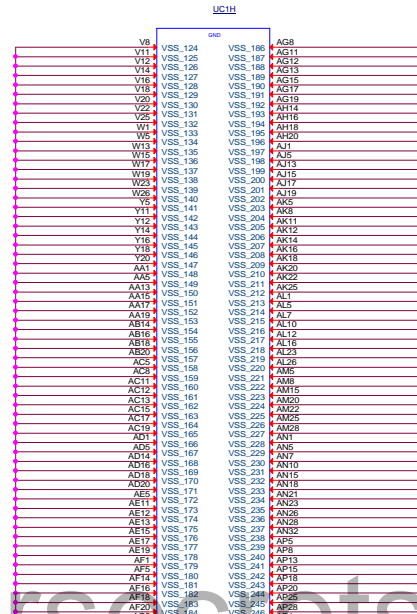
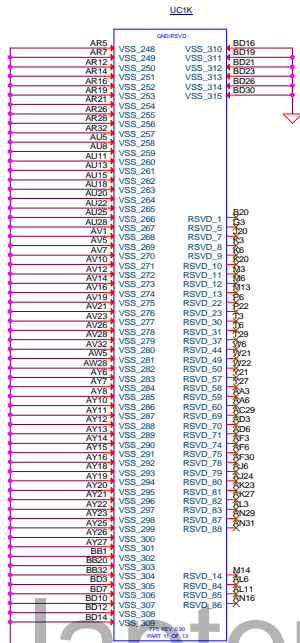
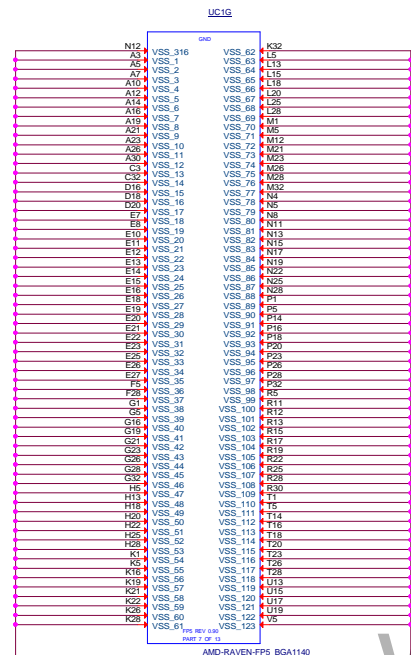
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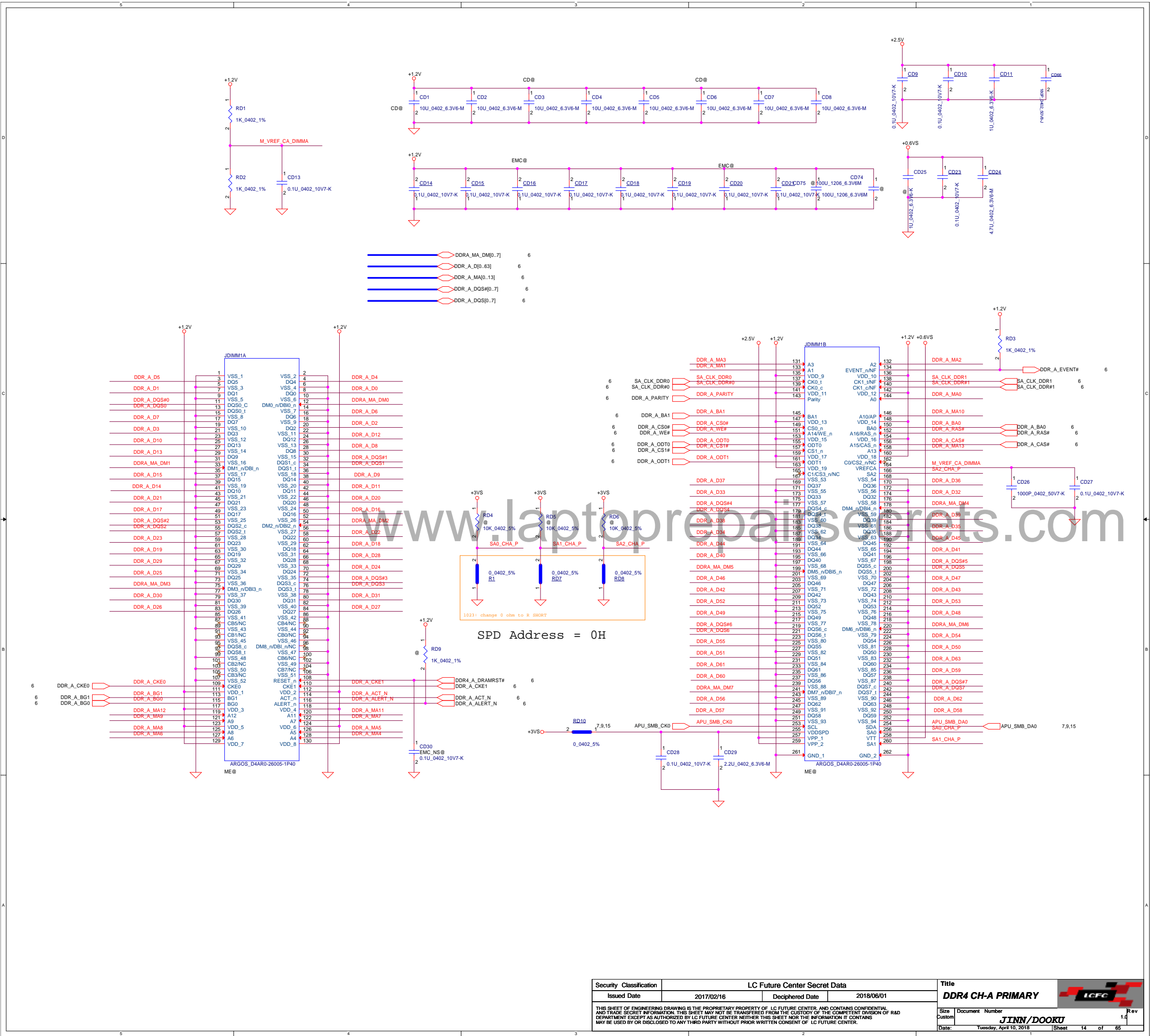


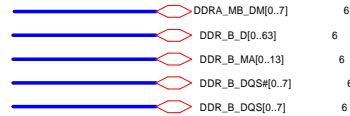
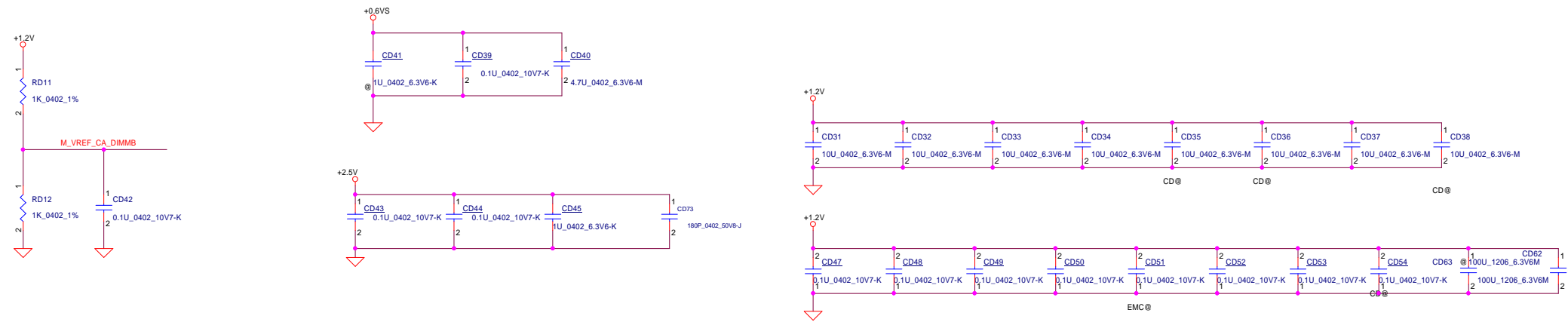
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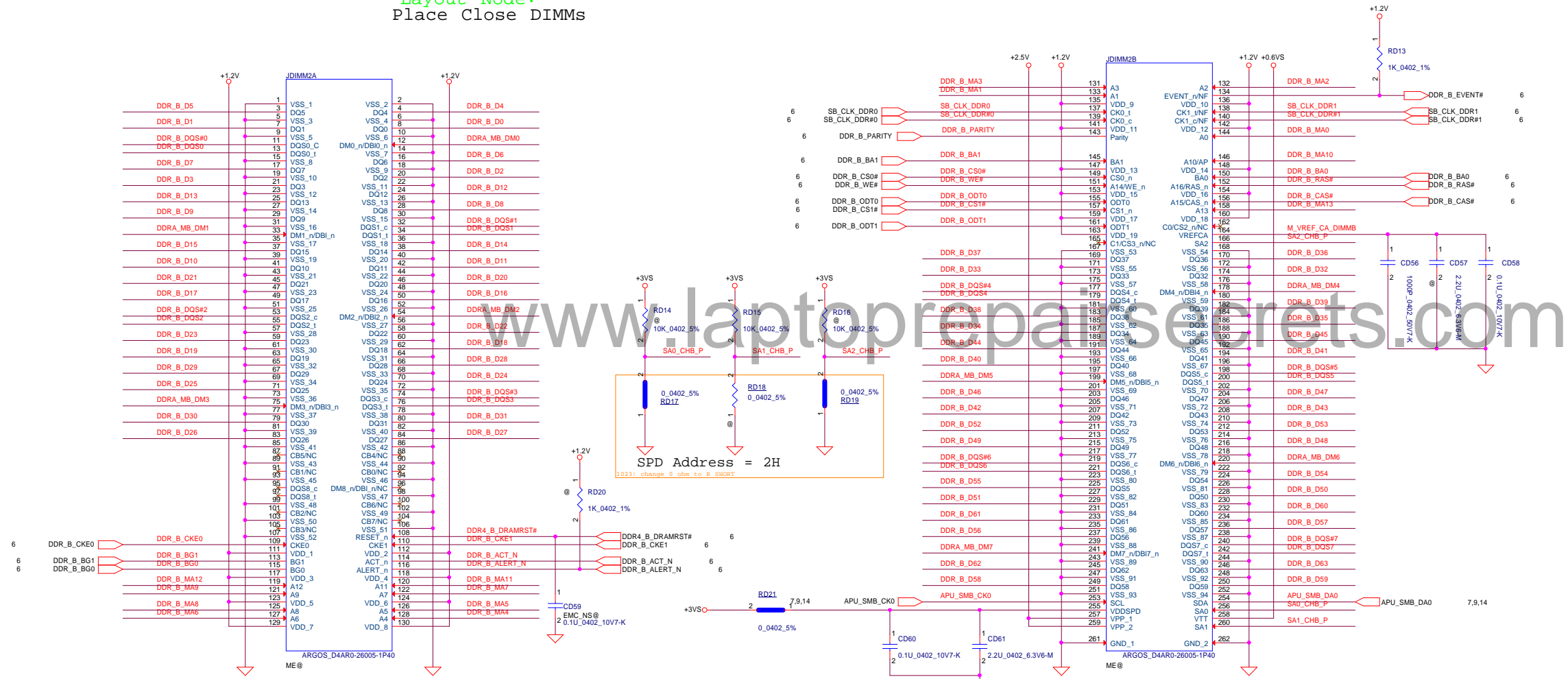


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				Date: Tuesday, April 10, 2018	Sheet 16 of 65

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				Sheet 17 of 65	

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				Date Tuesday, April 10, 2018	Rev 1.0
				Sheet 18	of 66

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A				A																														
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			Sheet 19	Rev 1.0																														
			of 65																															
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				Sheet 20 of 65	


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				Sheet 21 of 65	Rev 1.0


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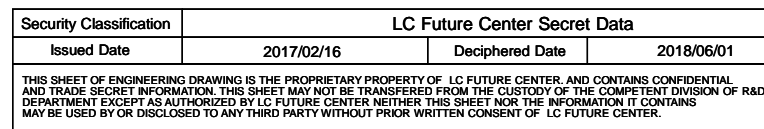
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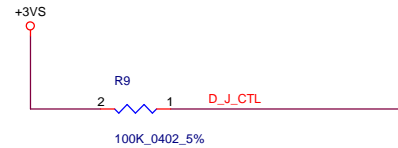
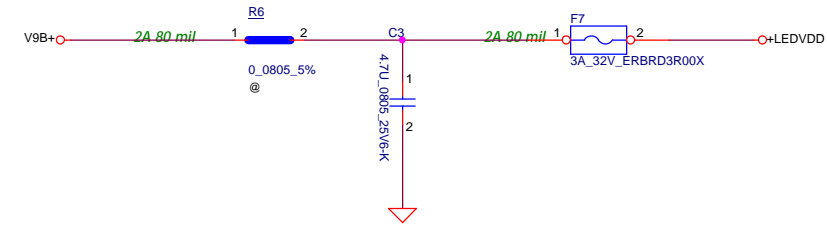
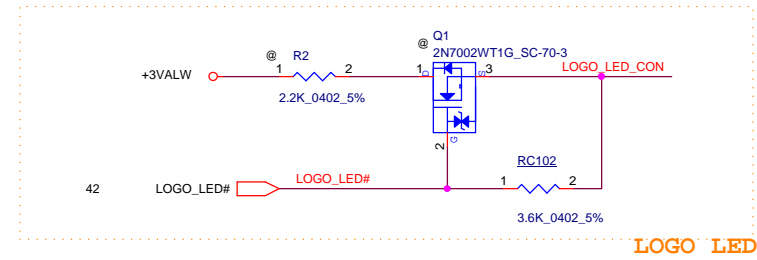
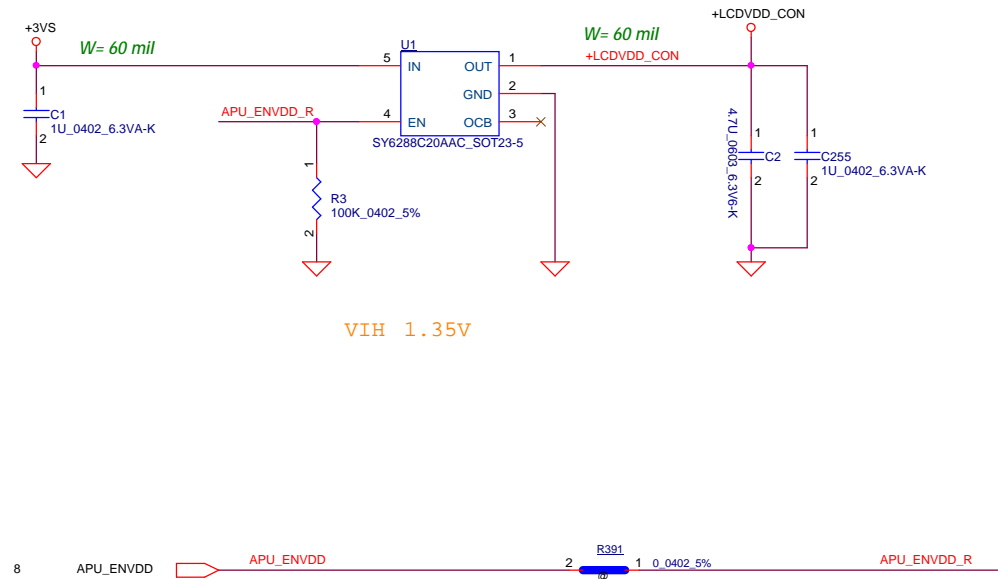
74	1.0V	GN0	75
72	1.0V	GN0	73
76	1.0V	GN0	77
68	R0SCUR124M1 (0000.3V)	PEDET (P0-PCH)	69
	Module Key	N/C	67
	Module Key	Module Key	
	Module Key	Module Key	
	Module Key	Module Key	
58	Reserved/MPO Clock	GN0	57
56	Reserved/MFG Data	REFCLK0	55
54	PCWAKEUP (0000.3V)	REFCLKIN	53
52	CLKREQ0 (0000.2V)	GN0	51
50	PERST0 (0000.3V)	PCF0a0	49
48	N/C	PCF0a0	47
46	N/C	GN0	45
44	N/C	PCF0a0	43
42	N/C	PCF0a0	41
40	N/C	GN0	39
38	DEVSPUR (0)	PER0a1	37
36	N/C	PER0a1	35
34	N/C	GN0	33
32	N/C	PCF0a1	31
30	N/C	PCF0a1	29
28	N/C	GN0	27
26	N/C	PER0a2	25
24	N/C	PER0a2	23
22	N/C	GN0	21
20	N/C	PER0a2	19
18	1.0V	PCF0a2	17
16	1.0V	GN0	15
14	1.0V	PCF0a3	13
12	1.0V	PER0a3	11
10	D0V/D0SG0 (0V)	GN0	9
8	N/C	PCF0a3	7
6	N/C	PCF0a3	5
4	1.0V	GN0	3
2	1.0V	GN0	1

74	0x0	CONF0_2 = 0b0	75
72	0x0	0b0	73
70	0x0	0b0	71
68	SUSCLK32MHz (10/3.3V)	CONF0_1 = N/C	69
	Module Key	N/C	67
	Module Key	Module Key	
	Module Key	Module Key	
	Module Key	Module Key	
58	Reserved for MFG Clock	0b0	57
56	Reserved for MFG Data	KECPUP	55
54	PERCLK# (10/3.3V)	KECPUP	53
52	CLKREQ# (10/3.3V)	0b0	51
50	PDRST# (10/3.3V)	PDRst0	49
48	N/C	PDRst0	47
46	N/C	0b0	45
44	N/C	PETst0	43
42	N/C	ACTst0	41
40	N/C	0b0	39
38	DEVSLP (1)	PDRst1	37
36	N/C	PDRst1	35
34	N/C	0b0	33
32	N/C	PETst1	31
30	N/C	PETst1	29
28	N/C	0b0	27
26	N/C	0b0	25
24	N/C	N/C	23
22	N/C	CONF0_0 = 0b0	21
20	N/C	Module Key	
	Module Key	Module Key	
	Module Key	Module Key	
	Module Key	Module Key	
10	DAS/OSM (10)	N/C	11
8	N/C	N/C	9
6	N/C	N/C	7
4	N/C	N/C	5
2	0b0	0b0	3
	0b0	CONF0_3 = 0b0	1

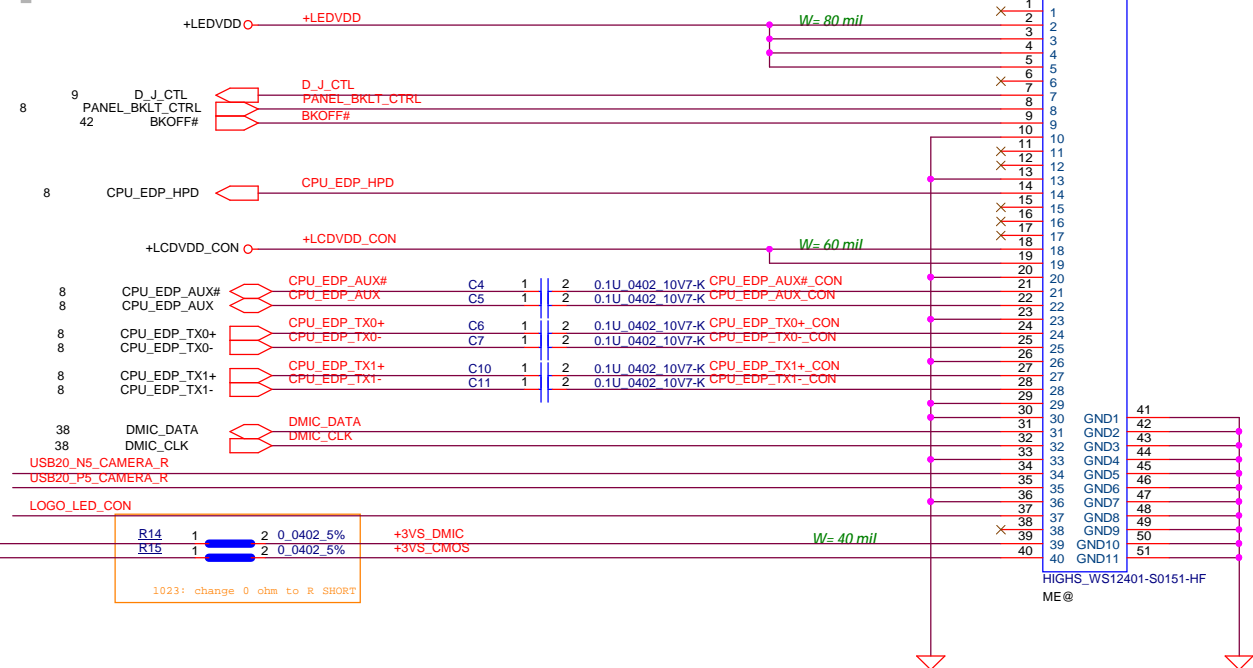
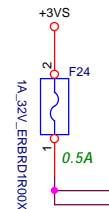
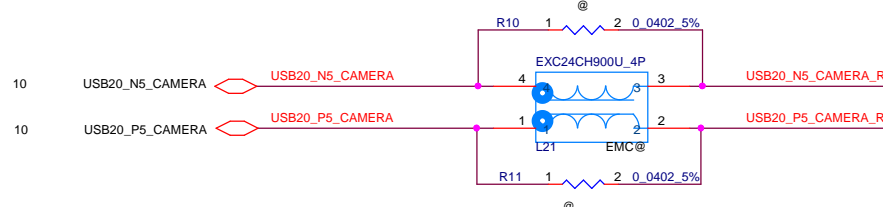
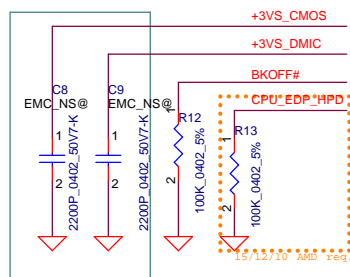
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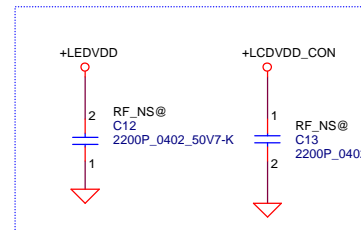
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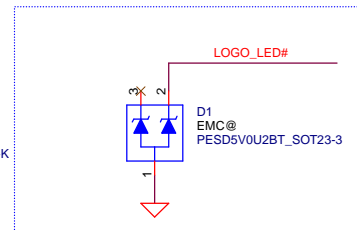
	Dooku	JINN
D_J_CTL	leave as NC	connect to GND by EDP cable



EMC request. Close to JLCD




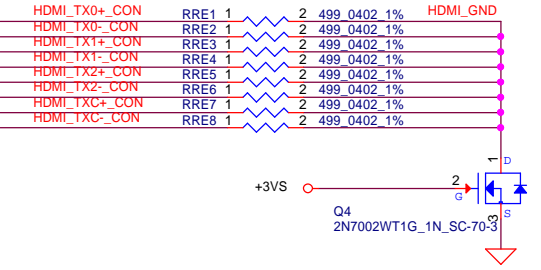
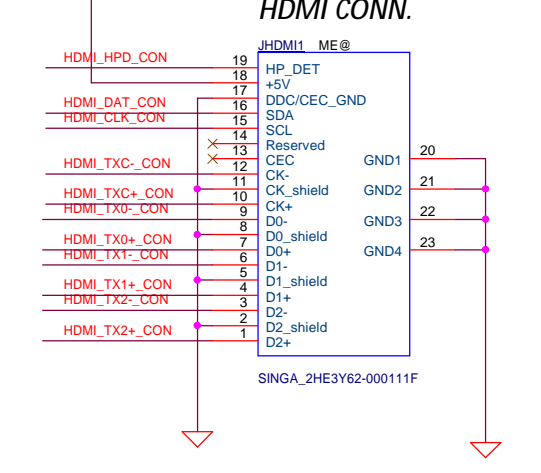
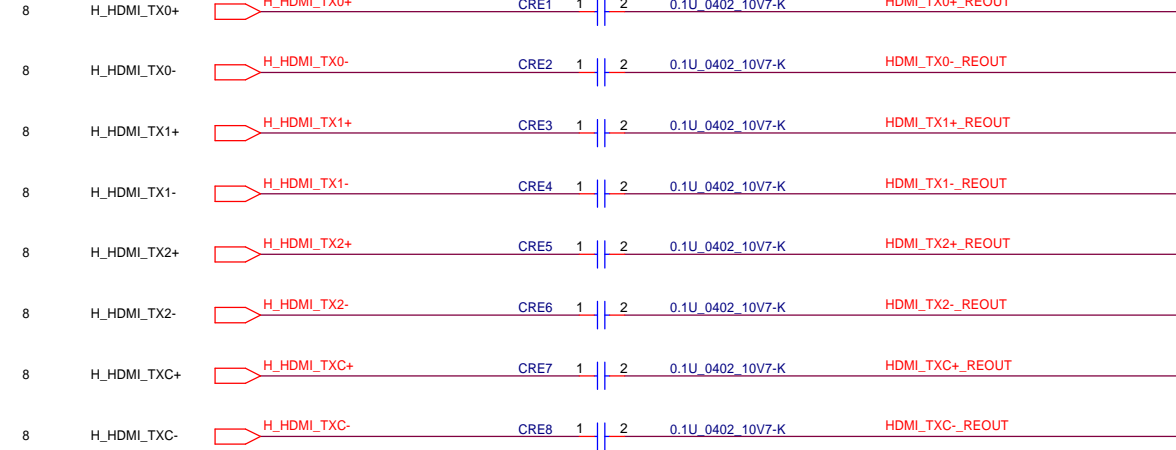
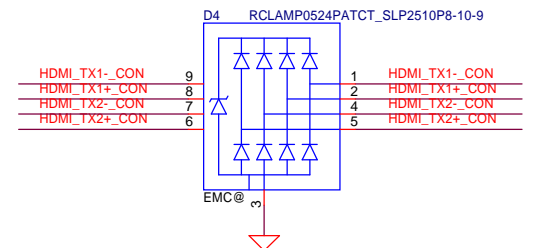
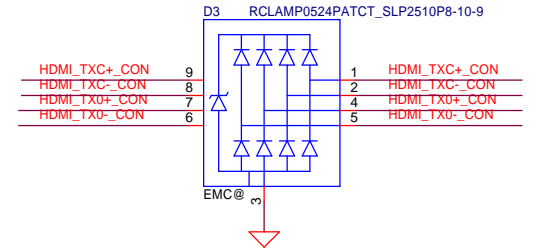
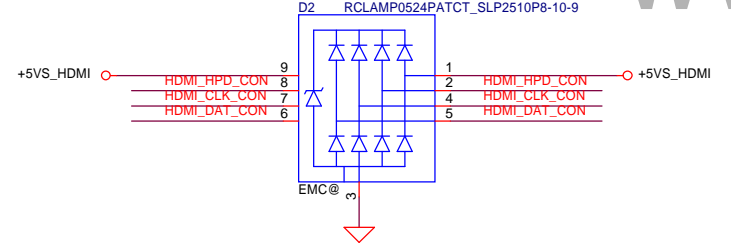
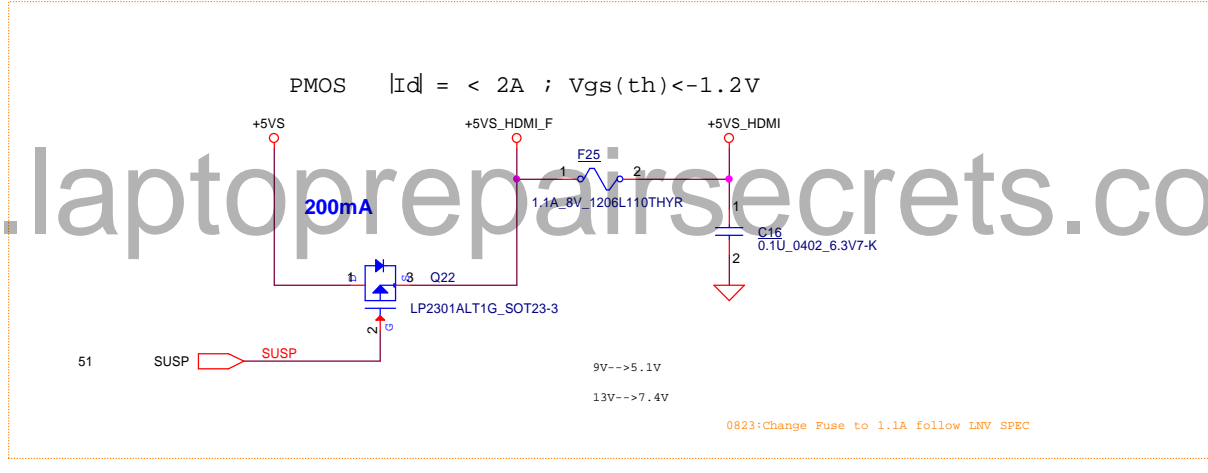
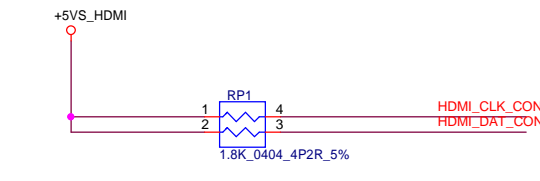
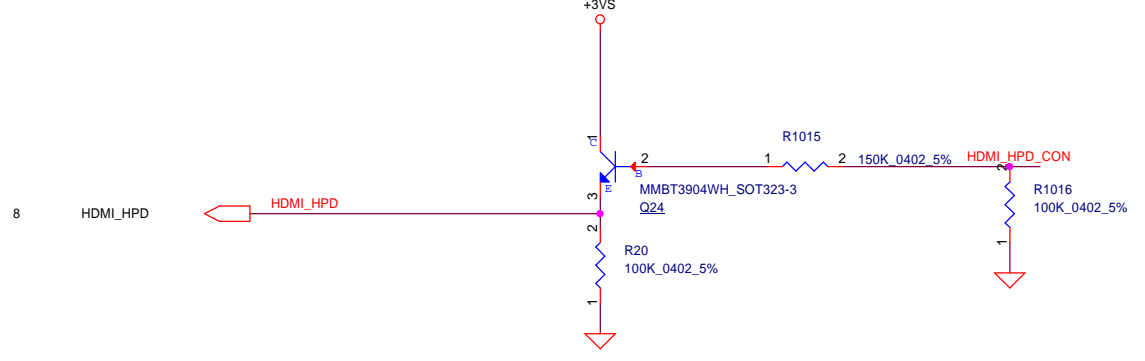
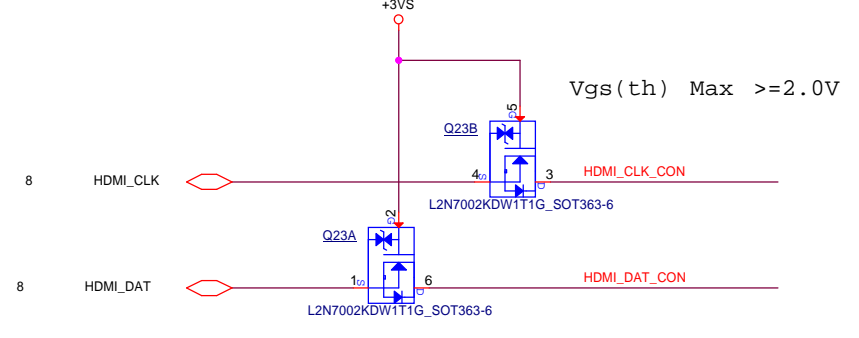
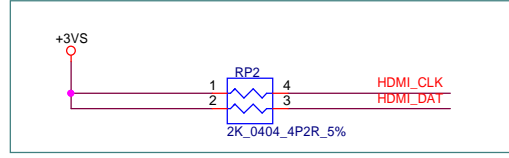
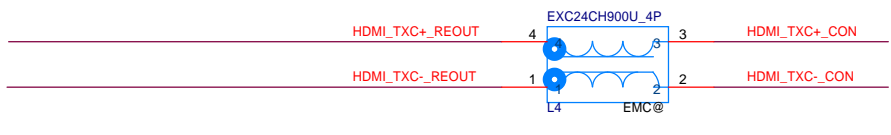
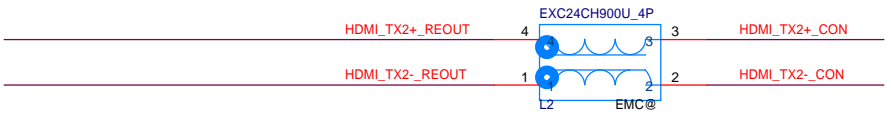
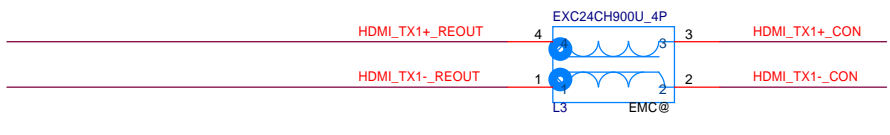
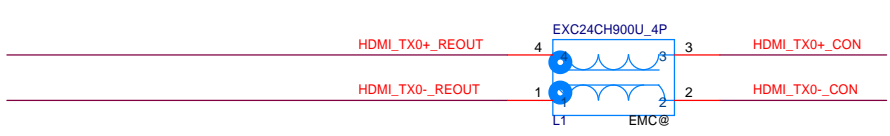
ESD request



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Date:	Tuesday, April 10, 2018	Sheet	26 of 65

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						Custom		JINN/DOOKU			1.0		
						Date:		Tuesday, April 10, 2018		Sheet	27	of	65

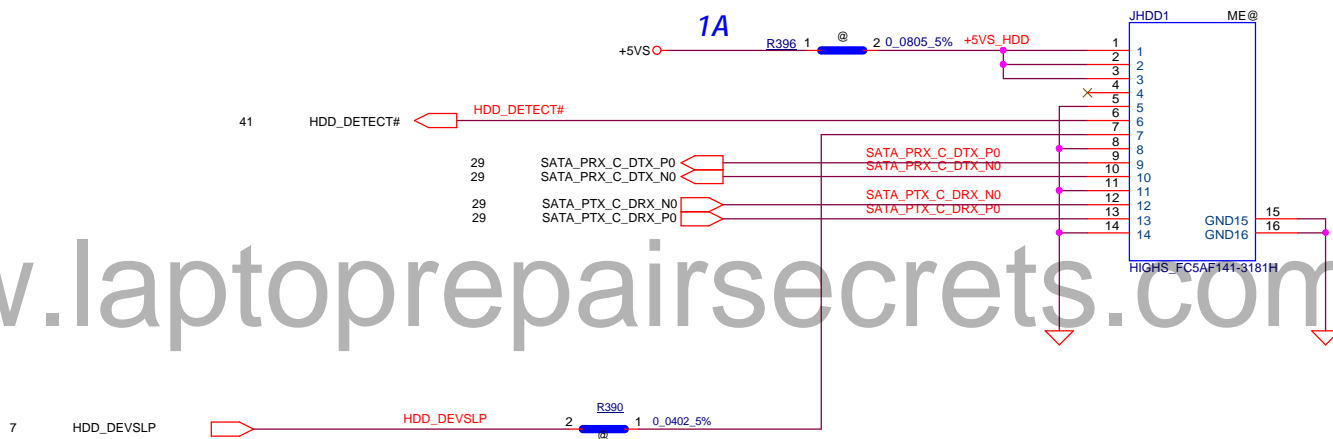
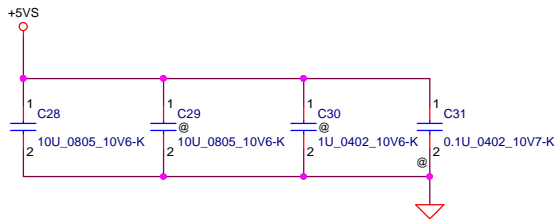


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		Custom		1.0	
Date:		Tuesday, April 10, 2018		Sheet 28 of 65	

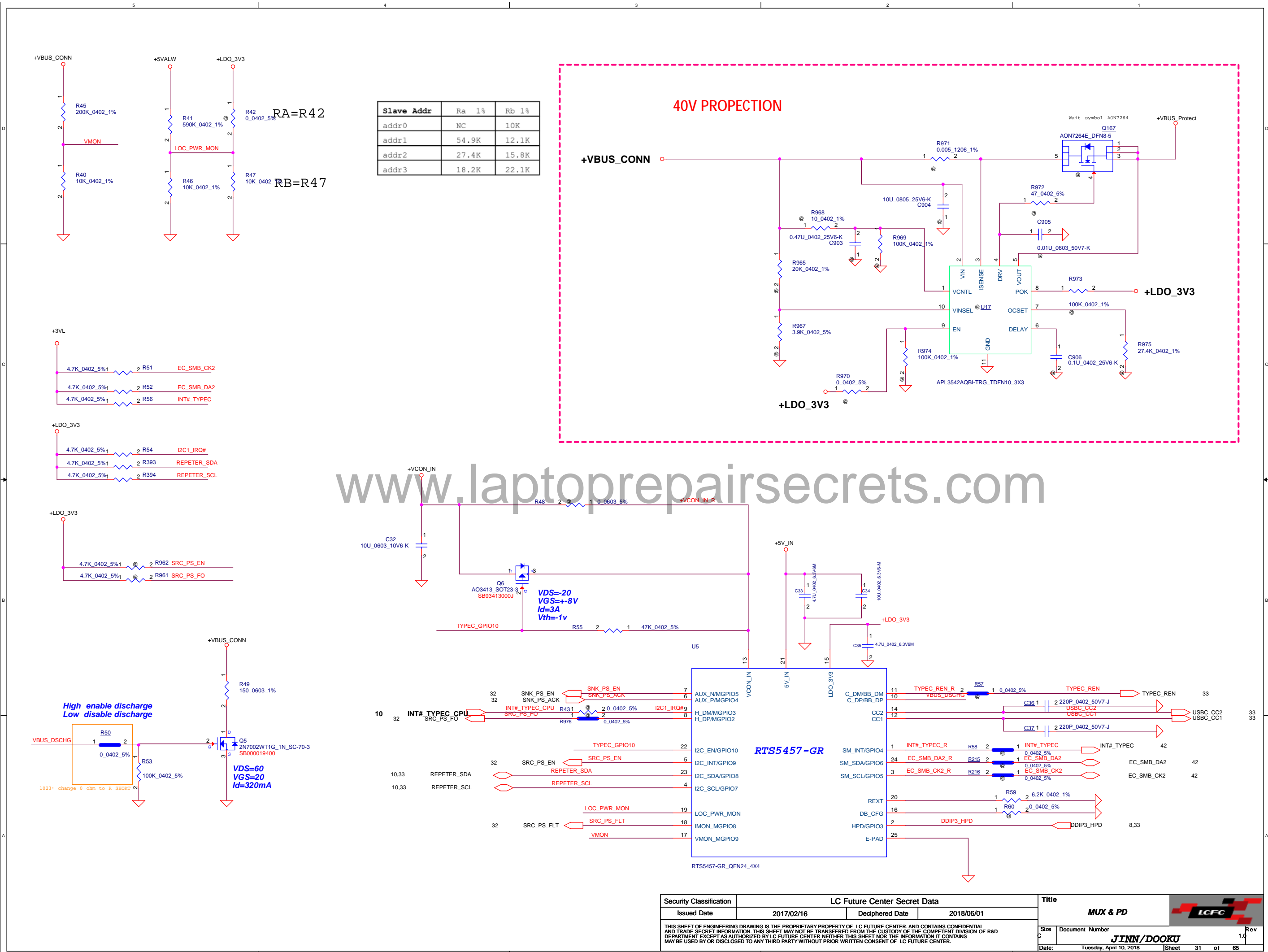
Rev
1.0

SATA HDD CONN.

SATA HDD CONN.



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Slave Addr	Ra 1%	Rb 1%
addr0	NC	10K
addr1	54.9K	12.1K
addr2	27.4K	15.8K
addr3	18.2K	22.1K

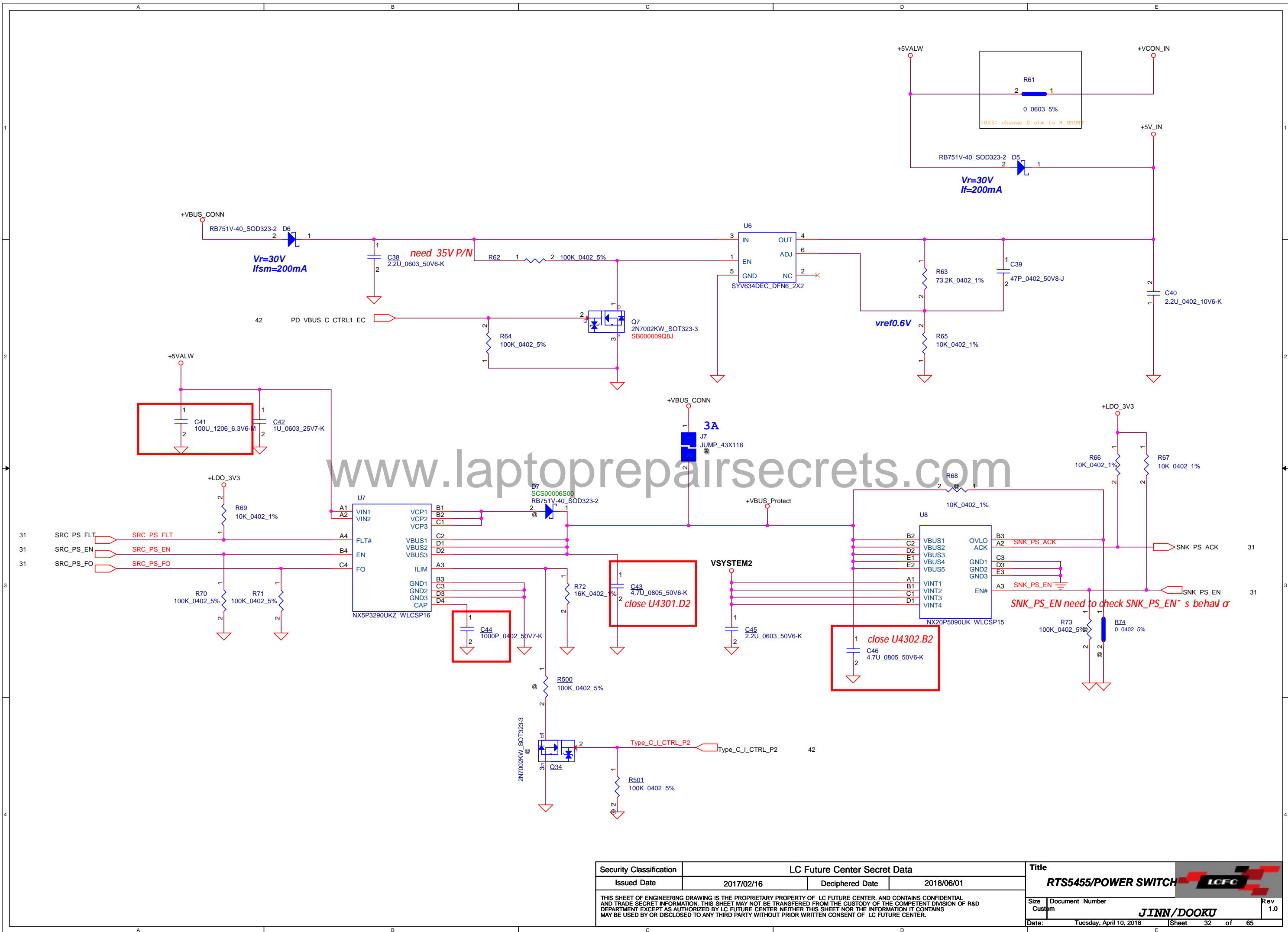
40V PROPECTION

+LDO_3V3

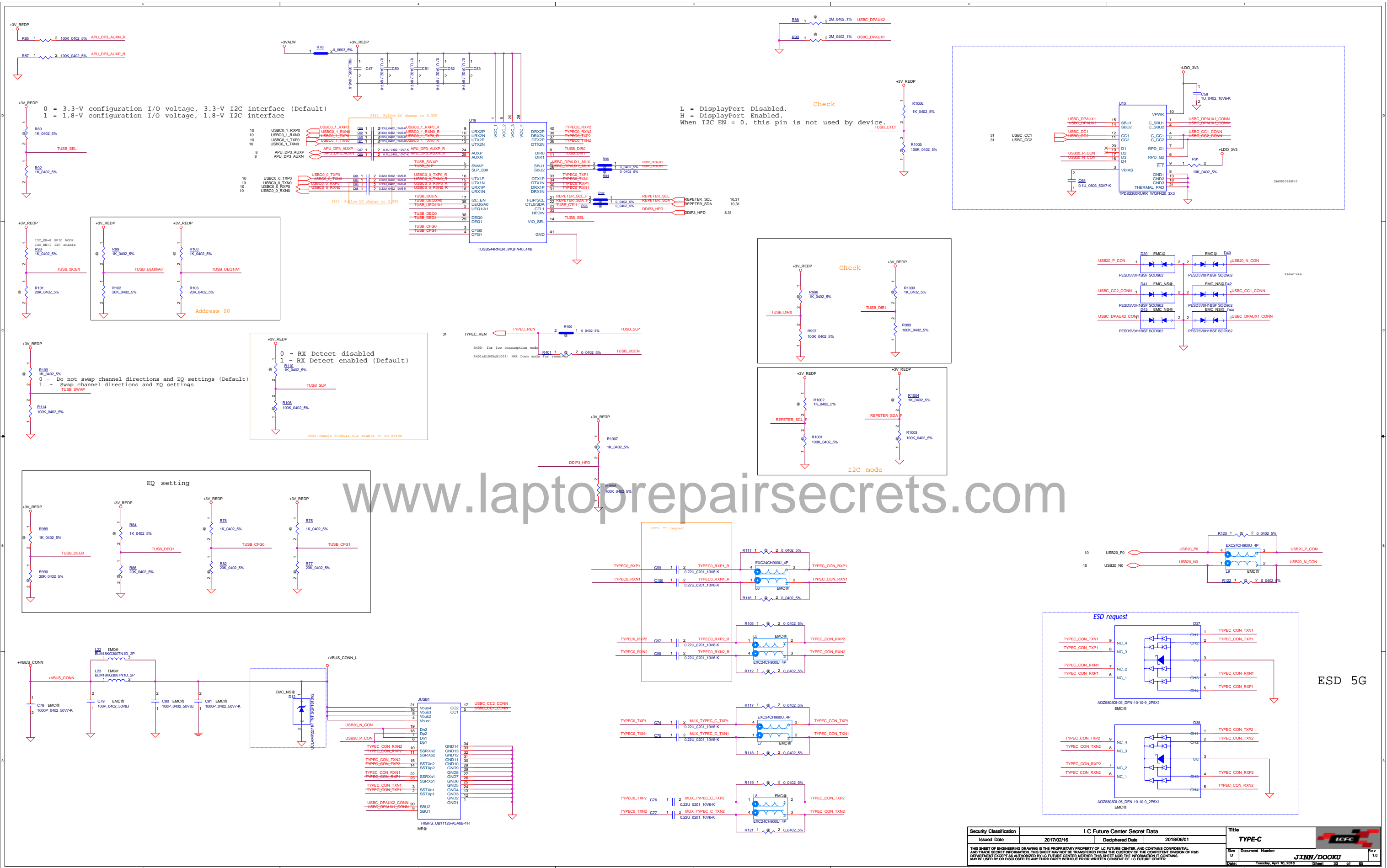
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MUX & PD		Rev
Size	Document Number	1.0
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Date:	Tuesday, April 10, 2018	Sheet 31 of 65



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				Custom	JINN/DOOKU
				Date:	Tuesday, April 10, 2018
				Sheet	32 of 65
				Rev	1.0



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Custom	JINN/DOOKU	1.0								
Date: Tuesday, April 10, 2018				Sheet 34 of 65						

On Board (LEFT-Front)

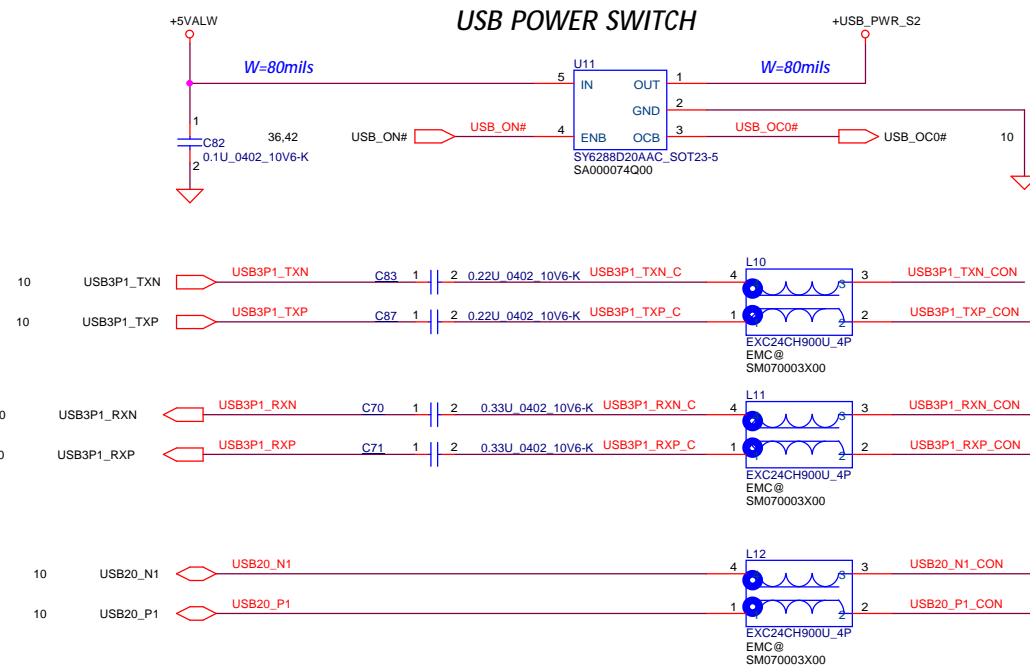
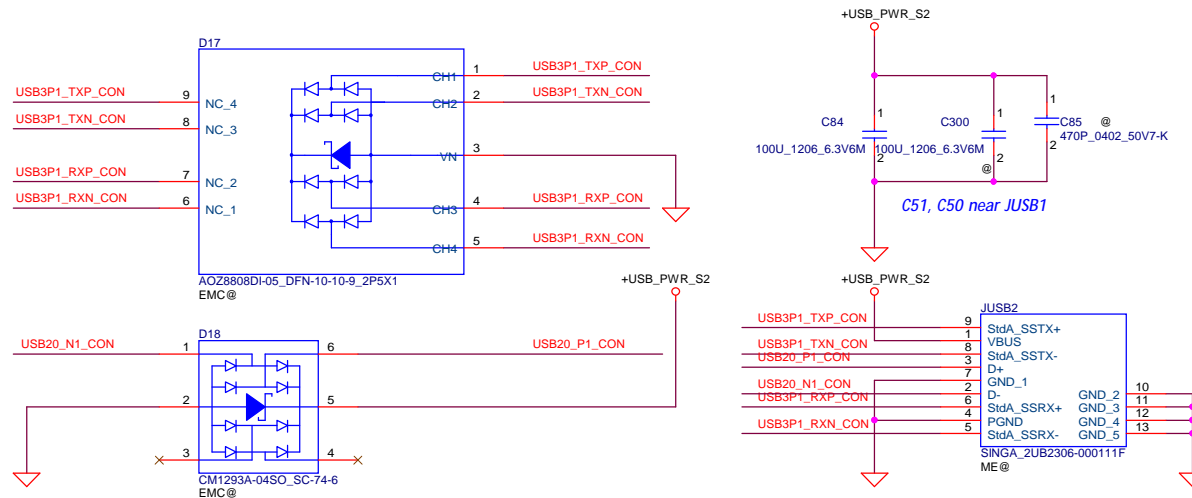
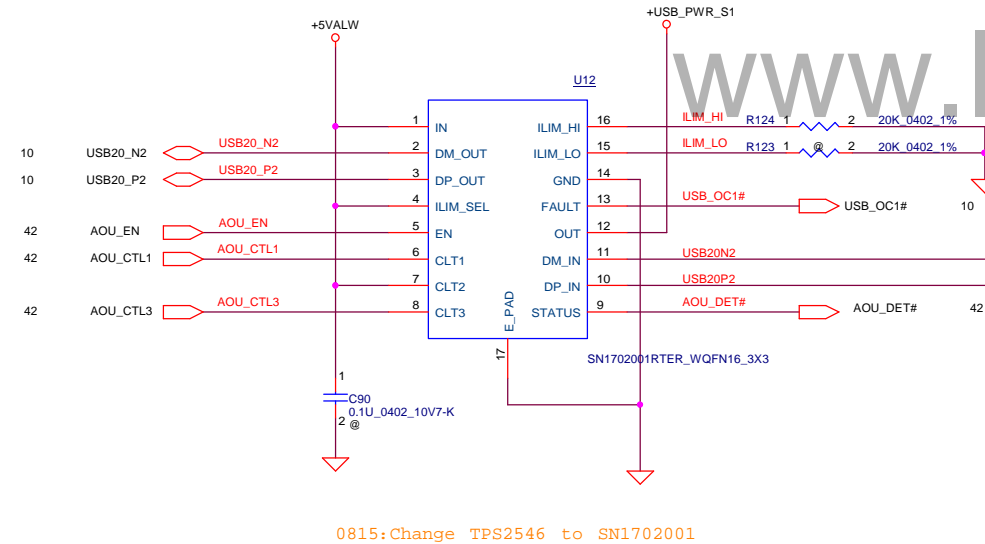


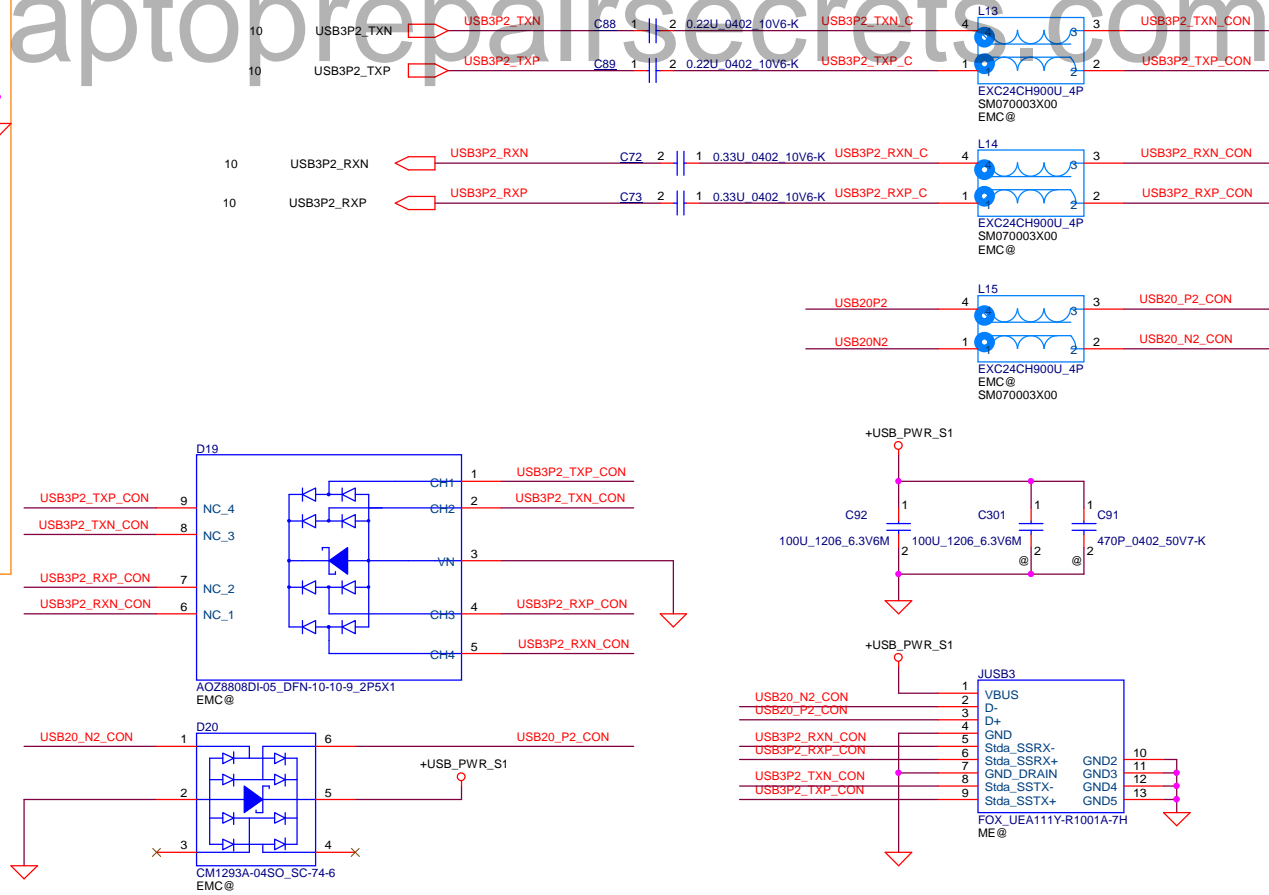
TABLE of POWER SWITCH (U6401)		
Vendor	LCFC P/N	Description
SILERGY	SA000074Q00	S IC SY6288D20AAC SOT23 5P POWER SWITCH
GMT	SA000079400	S IC G517F2T11U SOT-23 5P POWER SWITCH




On Board (LEFT-Back)

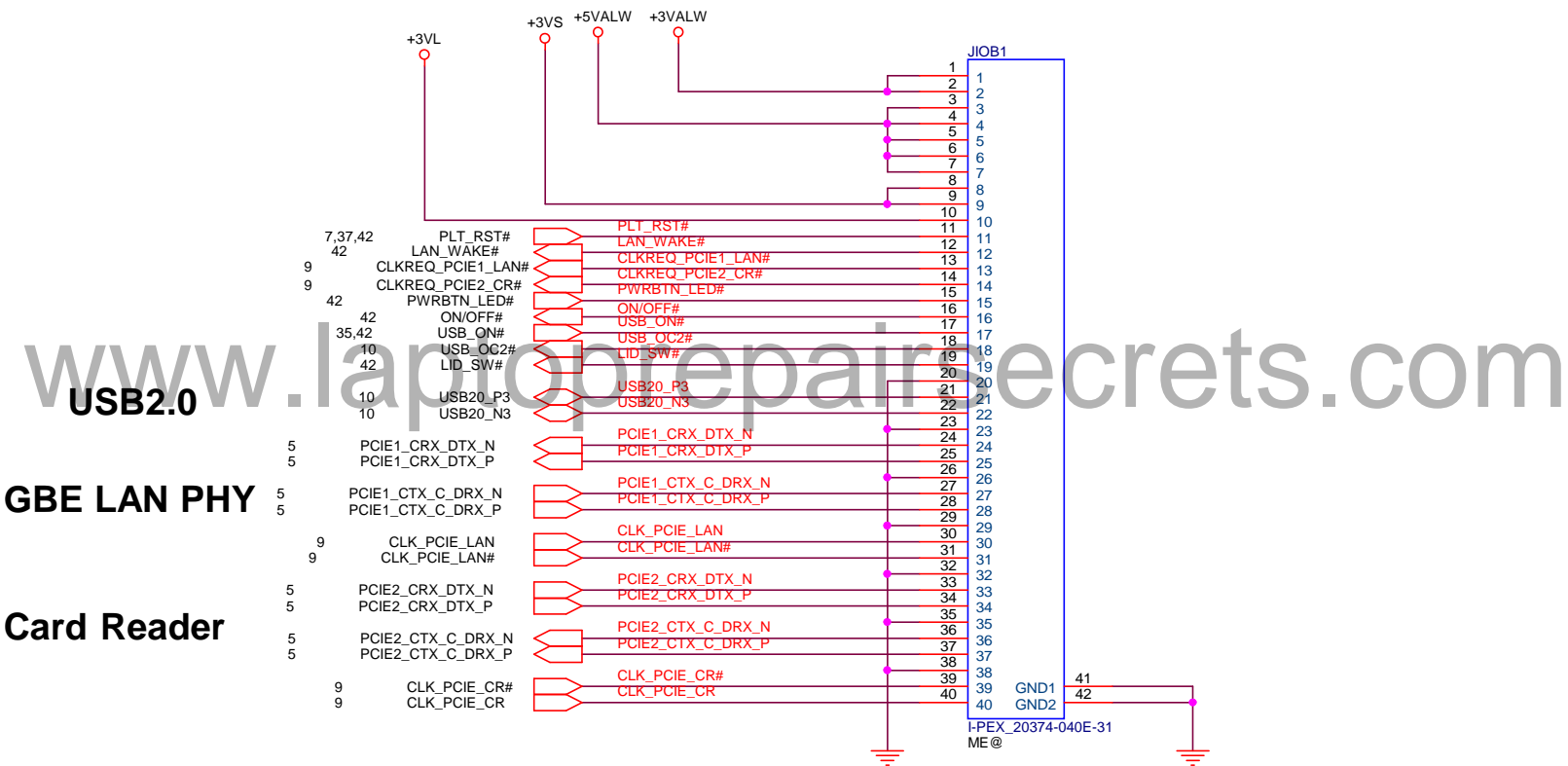


0815:Change TPS2546 to SN1702001



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				Date:	Tuesday, April 10, 2018	Sheet 35 of 65 Rev 1.

IO_40_Pin conn



TYPE-A NGFF CARD FOR WLAN

3.2H CONNECTOR

KEY A

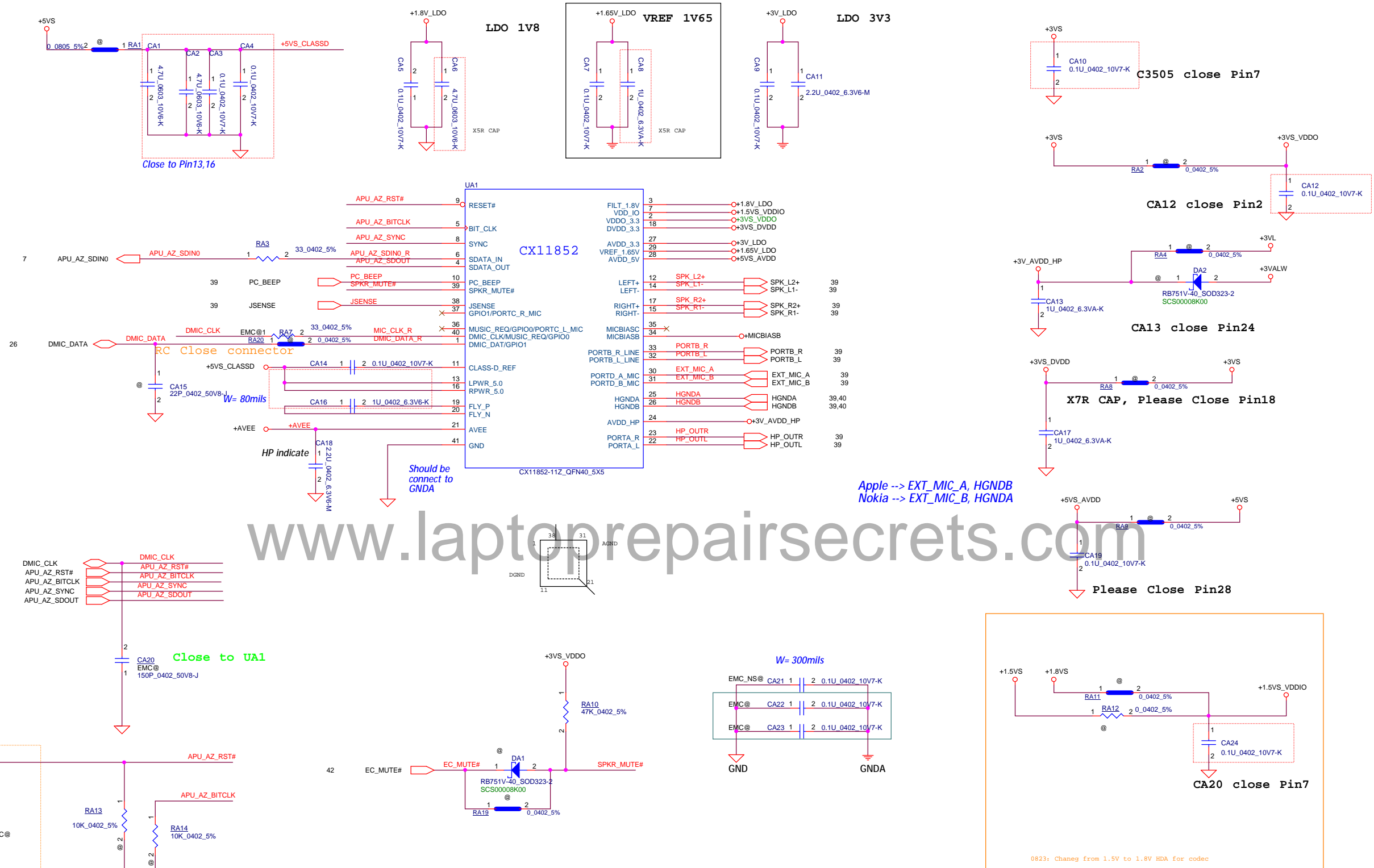
CHECK 0324

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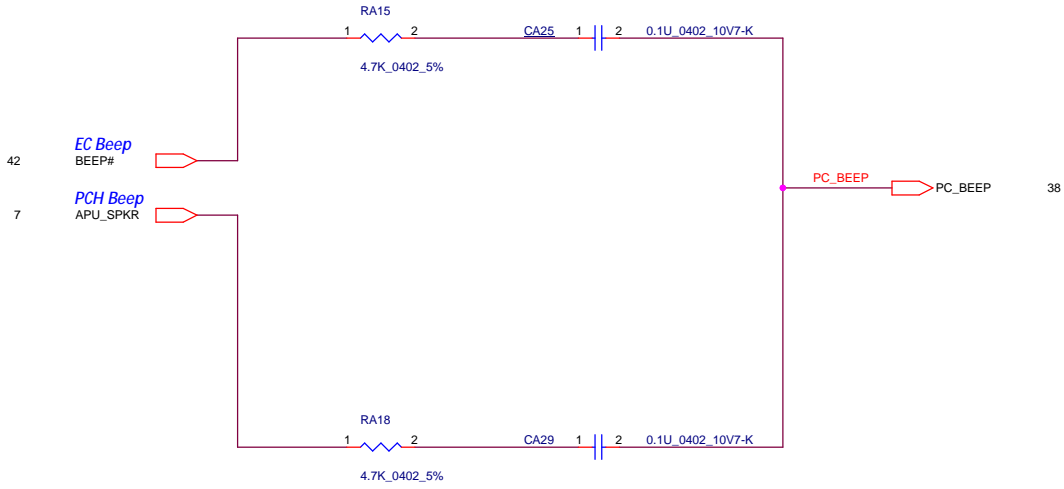
If support AOAC, NC R266;
if not support AOAC, stuff R266.

Vgs(th) = -0.65V (typ), -1V (max)
Rds(on) = 56 (typ), 80 (max) ; Vgs = -4.5V, Id = -3A

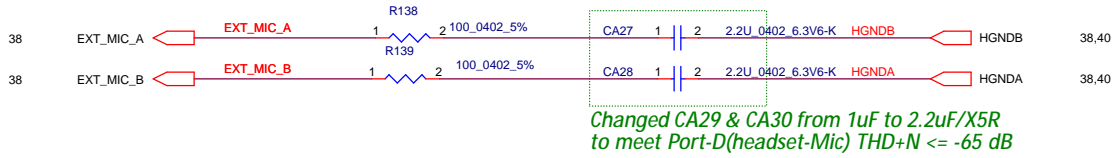
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Date:		Tuesday, April 10, 2018		Sheet	37 of 65



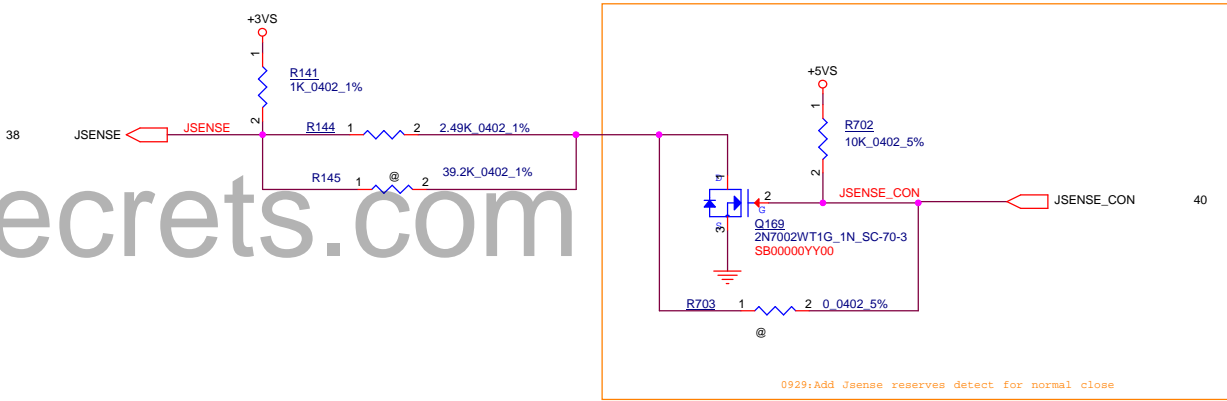
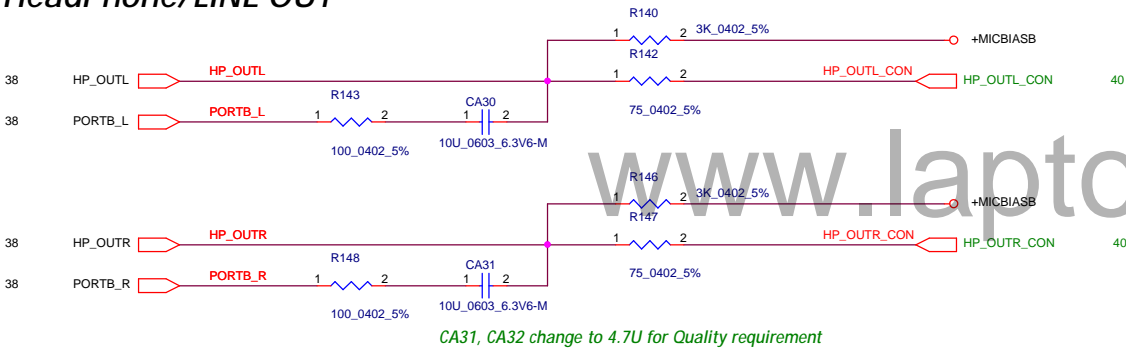
PC Beep



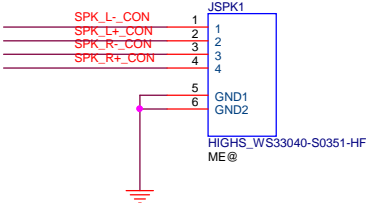
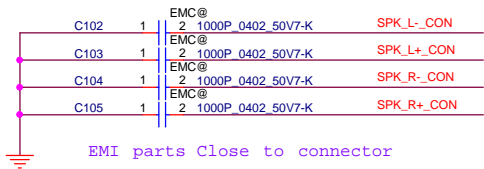
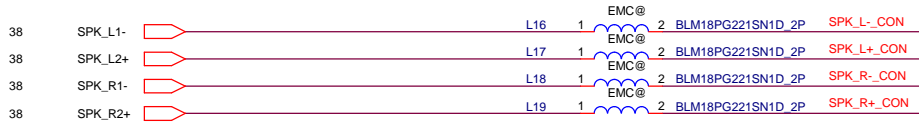
EXT. MIC/LINE IN Apple --> EXT_MIC_A, HGNDB
Nokia --> EXT_MIC_B, HGNDA



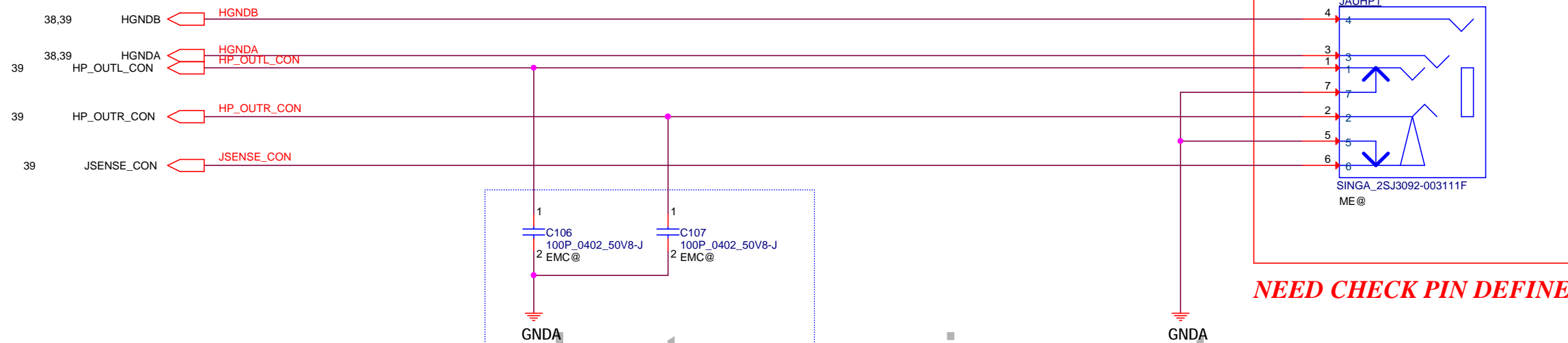
HeadPhone/LINE OUT



SPK CONN.



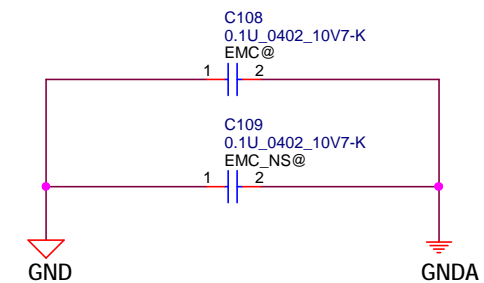
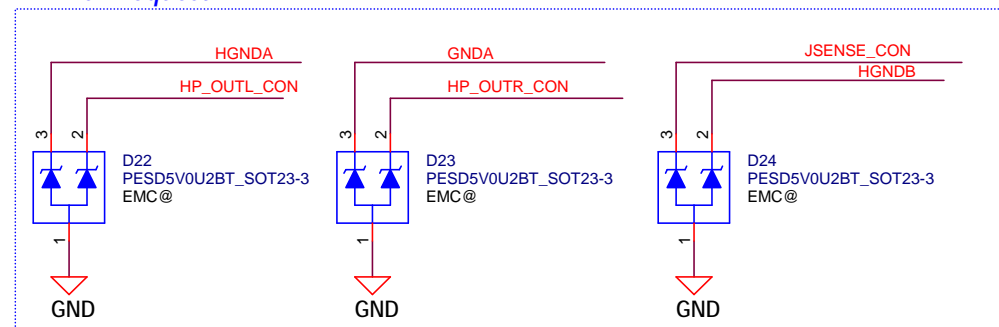
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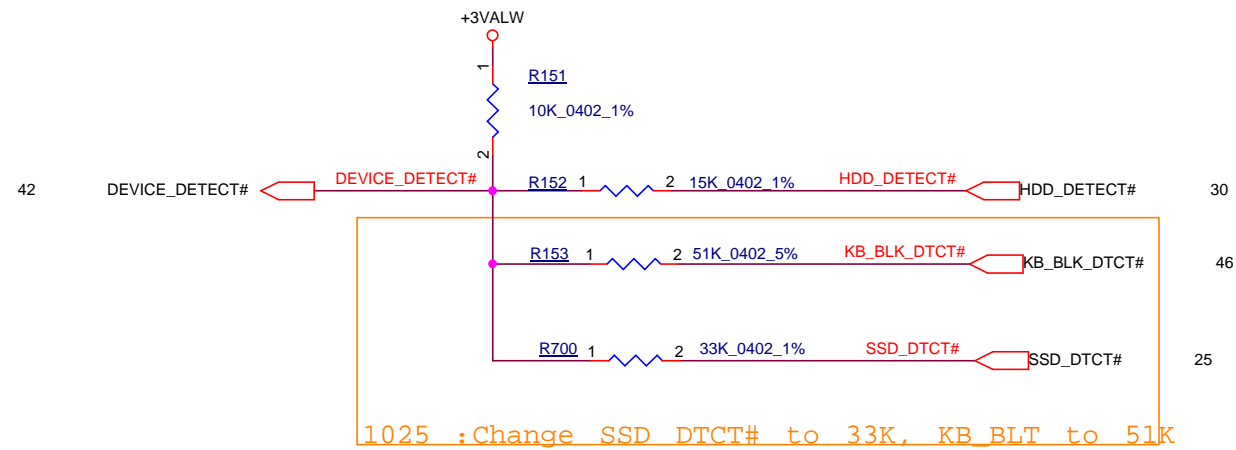
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Vendor suggestion. Reserve for EMI.
Close to JAUHP.

ESD request




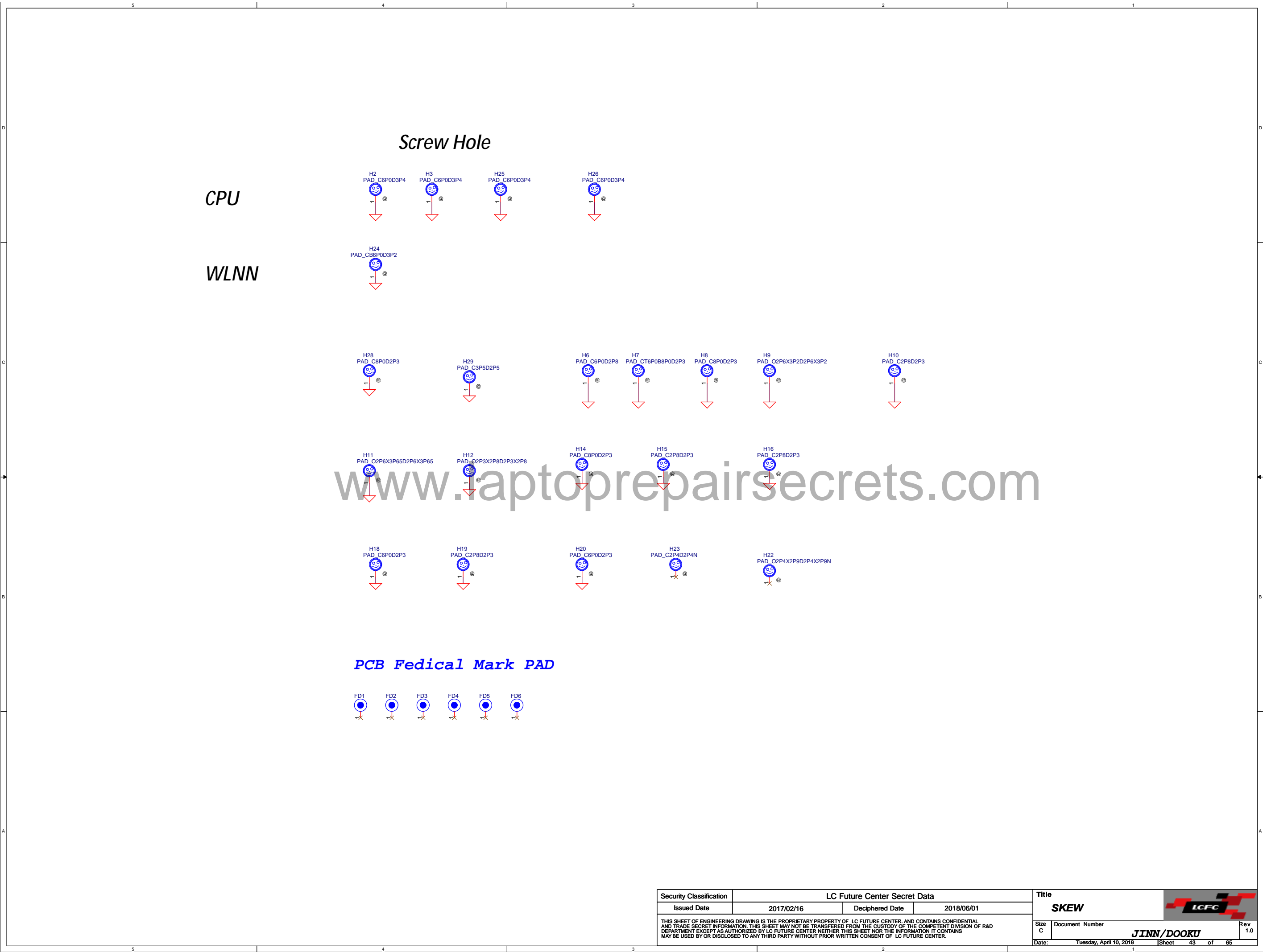
Security Classification	LC Future Center Secret Data			Title	
Issued Date	2017/02/16	Deciphered Date	2018/06/01	AUDIO JACK SENSE	
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				Date: Tuesday, April 10, 2018	Rev 1.0
				Sheet 40 of 65	



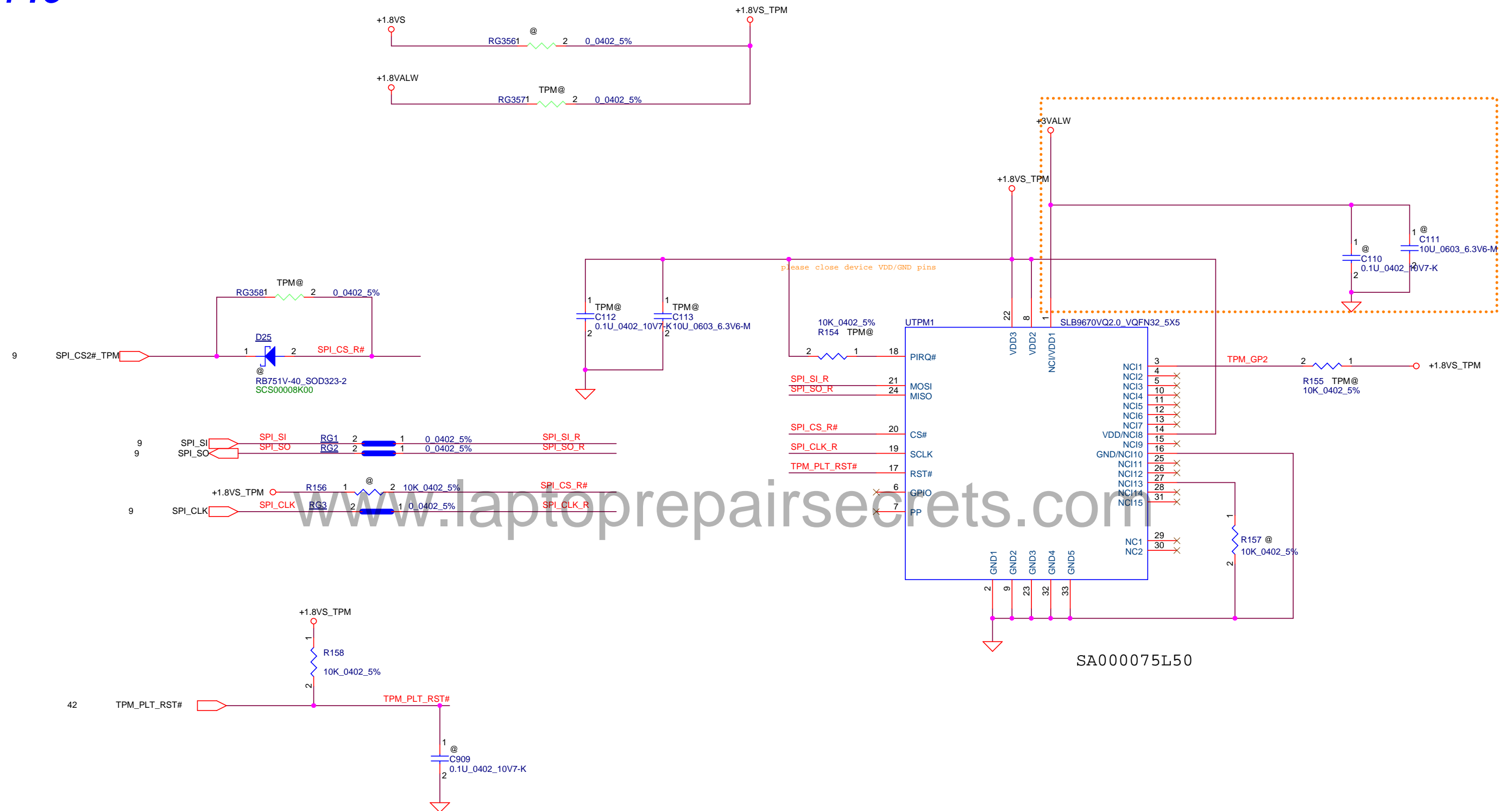
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Vcc	3.3V					
R151	10K +/- 5%					
DEVICE_DETECT1#	1.98V	1.675V	1.772V	2.759V	2.533V	3.3V
HDD_DETECT#	V	V	V	X	X	X
KB_BLK_DTCT#	X	V	X	X	V	X
WWAN_DTCT#	X	X	V	V	X	X

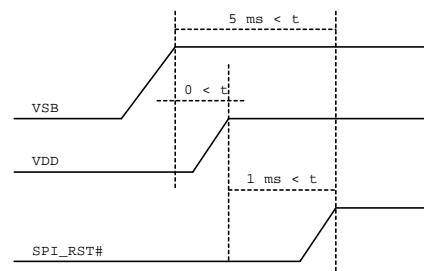
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Issued Date	2017/02/16	Deciphered Date	2018/06/01	DEVICE_DETECT# TABLE	
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				Date: Tuesday, April 10, 2018	Rev 1.0
				Sheet 41 of 65	




TPM IC




NOTE:
Check timing sequence in SDV phase.

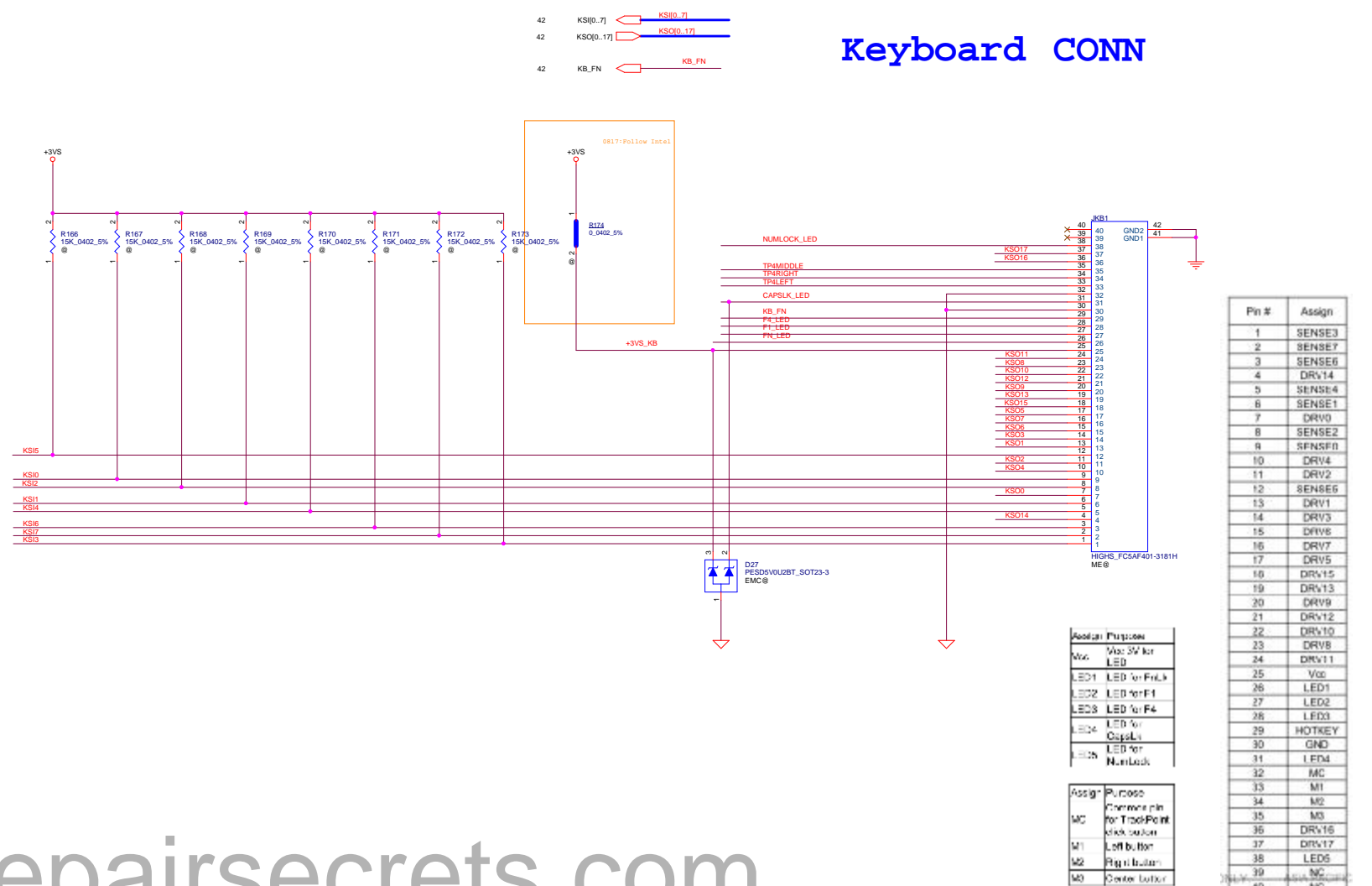
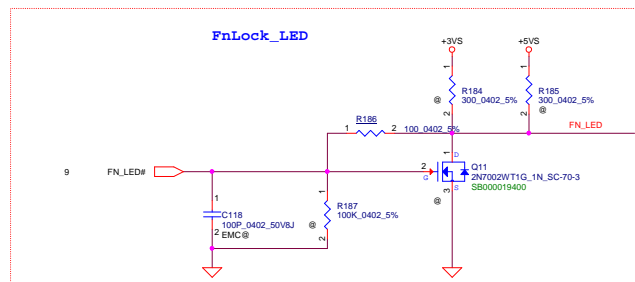
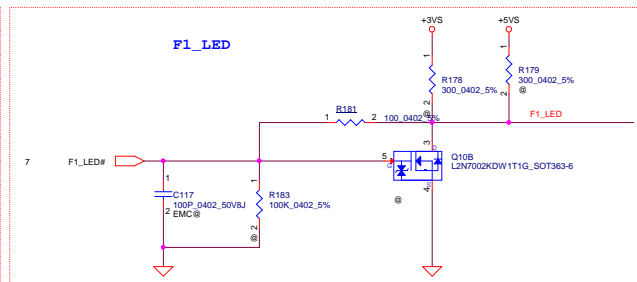
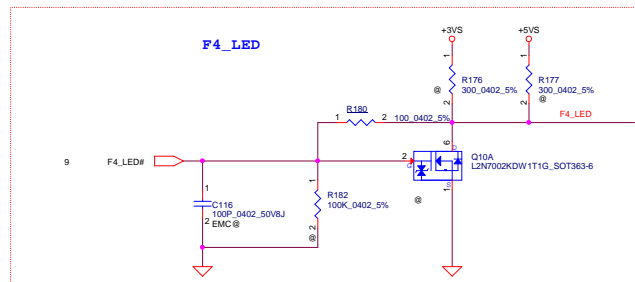
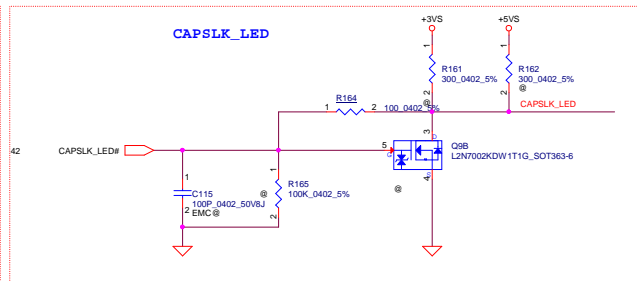
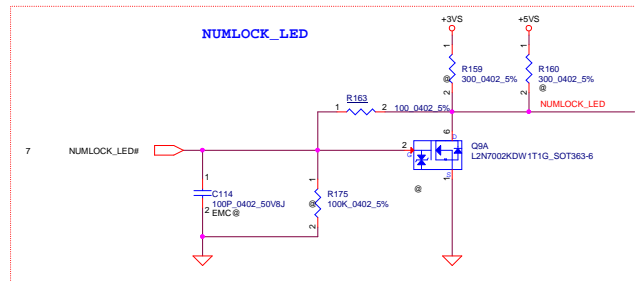


- NOTE:
- 1) It is recommended to connect the TPM to the system's standby voltage to improve performance.
 - 2) SPI_RST# must be asserted for at least 5 msec after VSB power-up.
 - 3) VSB may come up anytime before VDD power-up, but not after VDD power-up.
 - 4) SPI_RST# may be asserted together with VDD power negation, but should not at any point exceed 0.5V above the VDD power level.

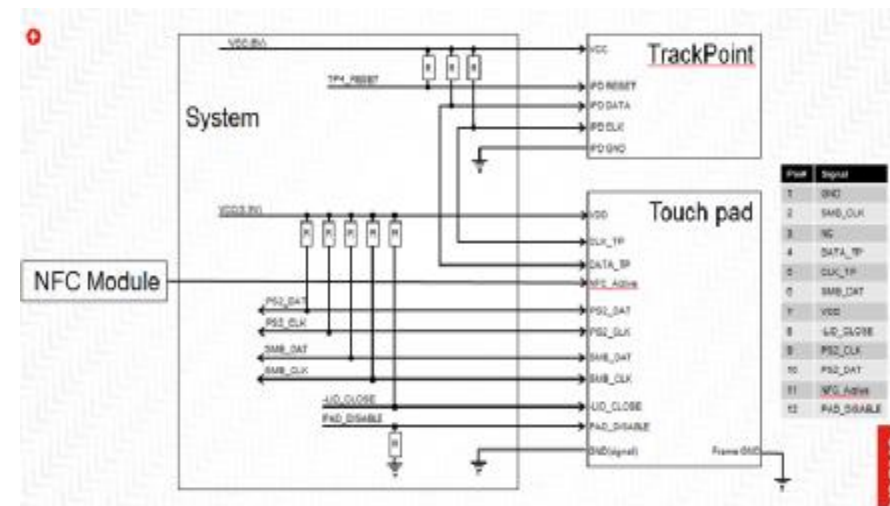
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Issued Date	2017/02/16	Deciphered Date	2018/06/01		
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Date: Tuesday, April 10, 2018				Sheet 44 of 65	

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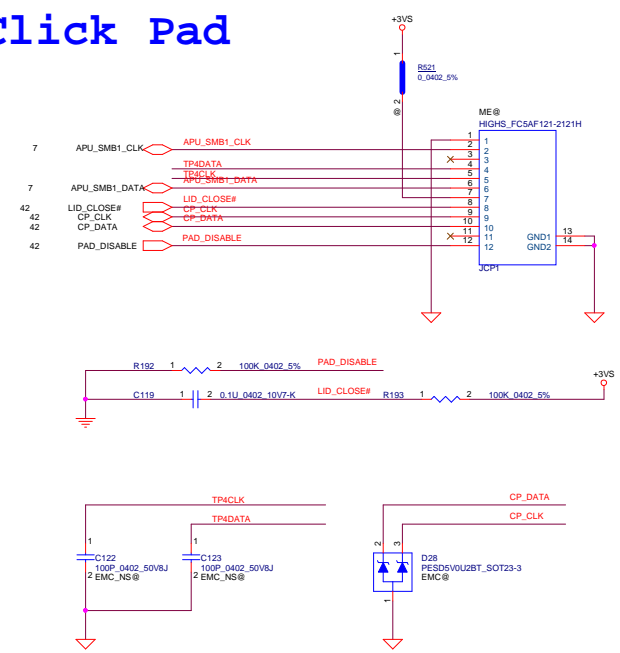
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		Date:	Tuesday, April 10, 2018	Sheet	45 of 65
		JINN/DOOKU		Rev	1.0



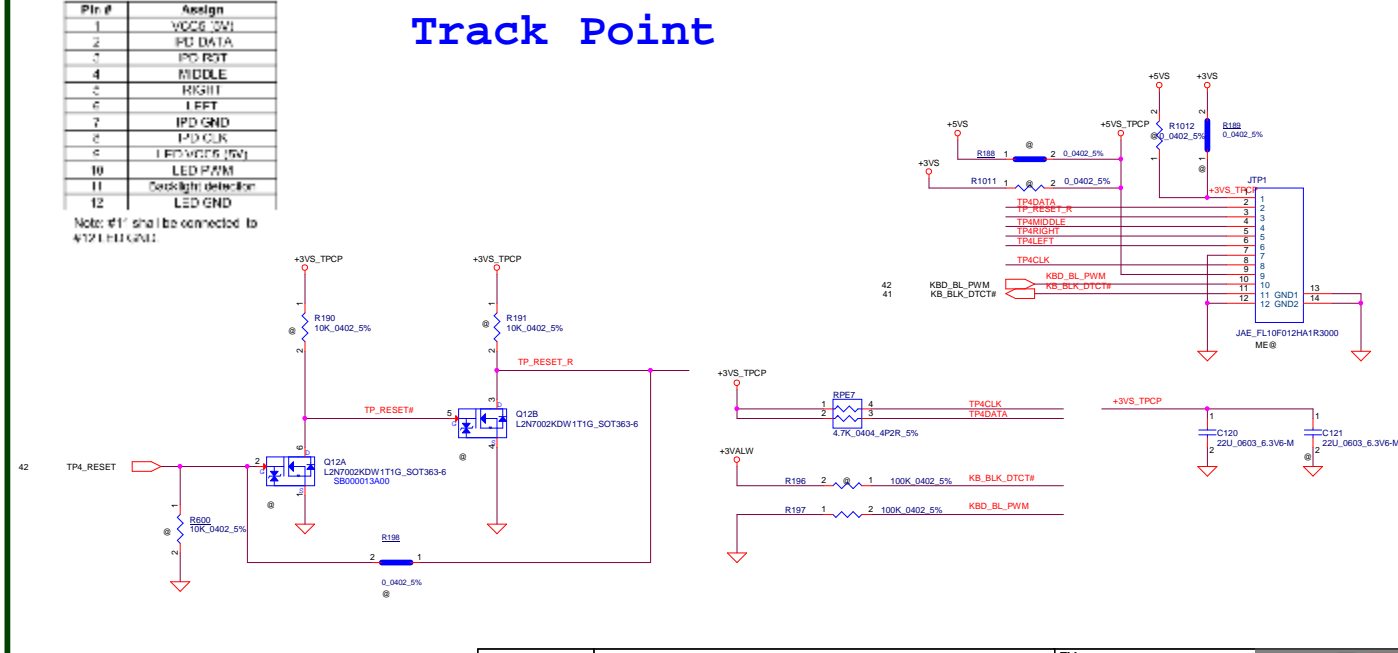
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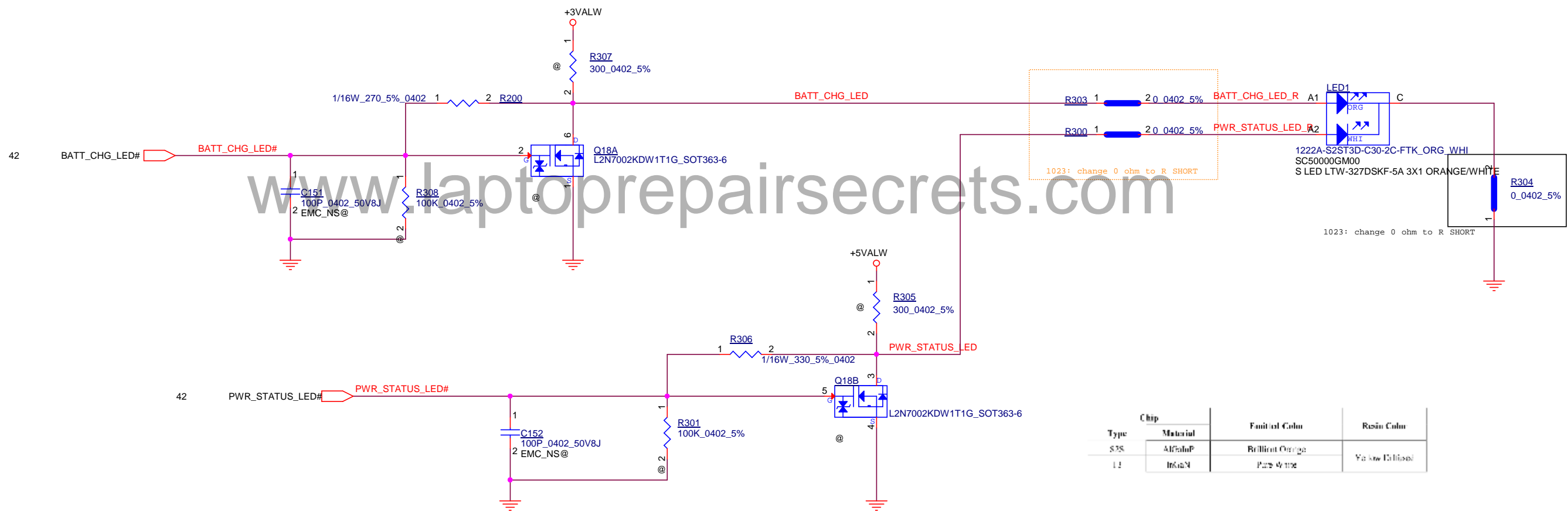
Click Pad



Track Point



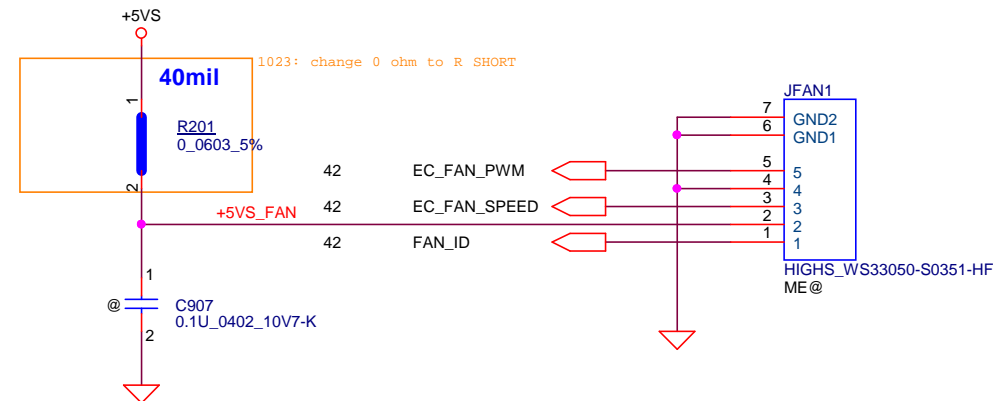
POWER ADAPTER Bi-COLOR(ORANGE/WHITE)



Chip		Footed Color	Resin Color
Type	Material		
S2S	Aluminum	Brilliant Orange	Yellowish
11	Insulation	Part of the	Yellowish

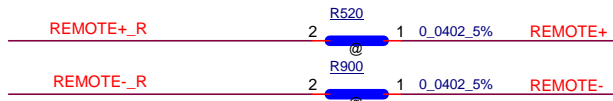
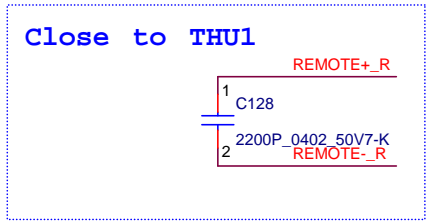
Security Classification				LC Future Center Secret Data		Title	
Issued Date				2017/02/16	Deciphered Date	2018/06/01	POWER LED
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						JINN/DOOKU	1.0
				Date:		Tuesday, April 10, 2018	Sheet 47 of 65

FAN CONN.

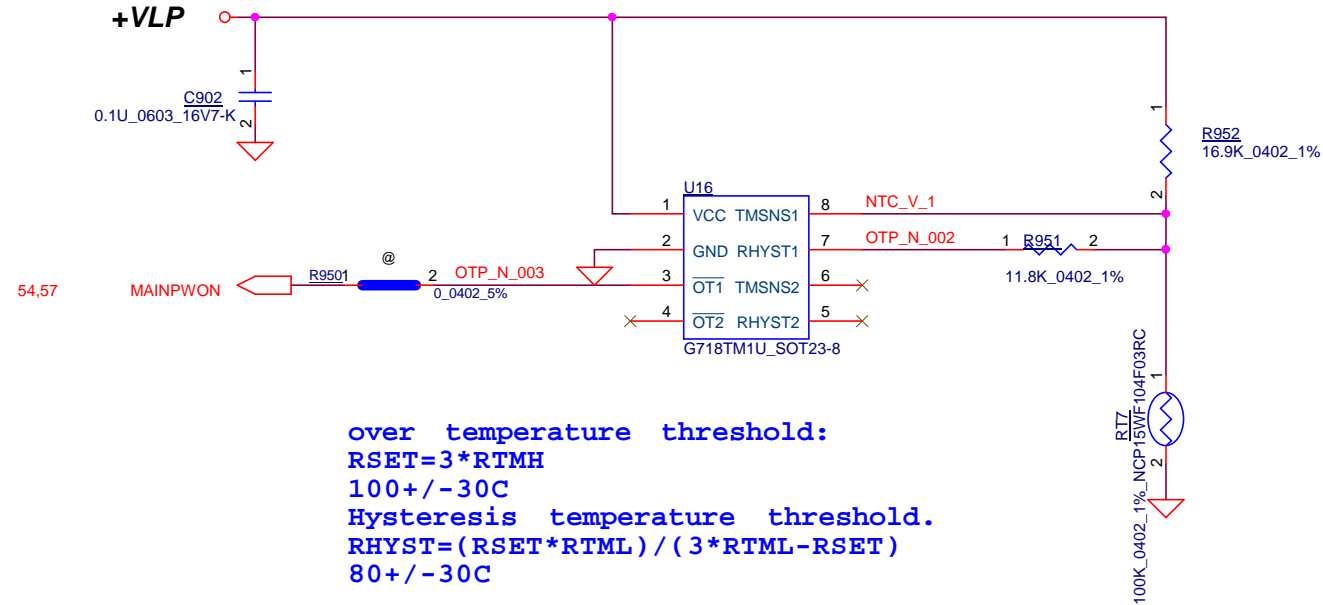
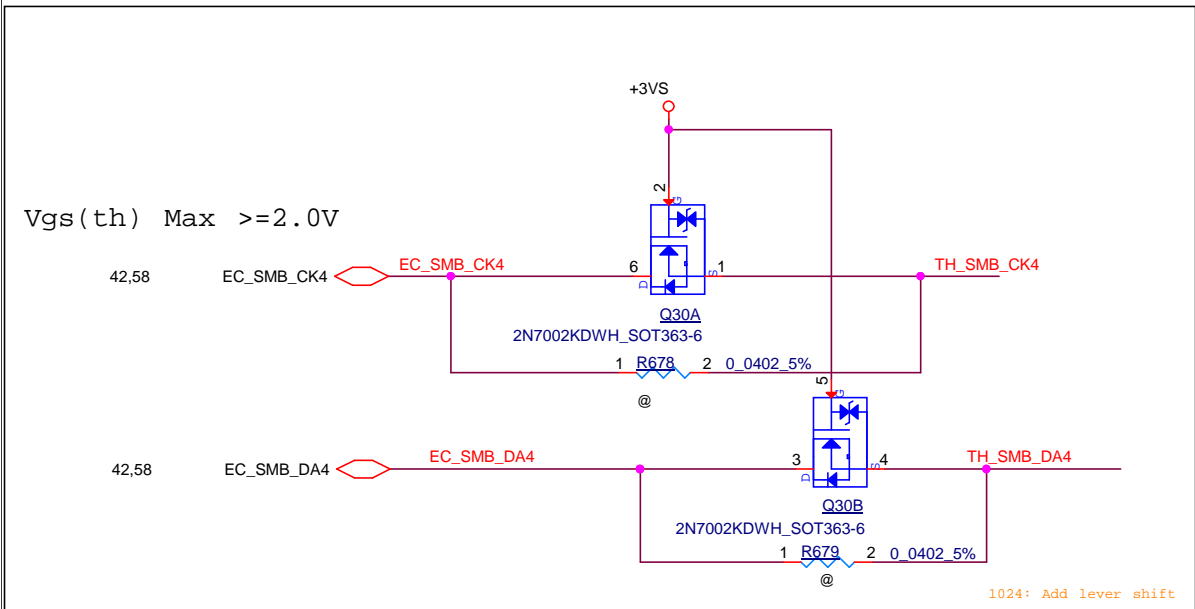
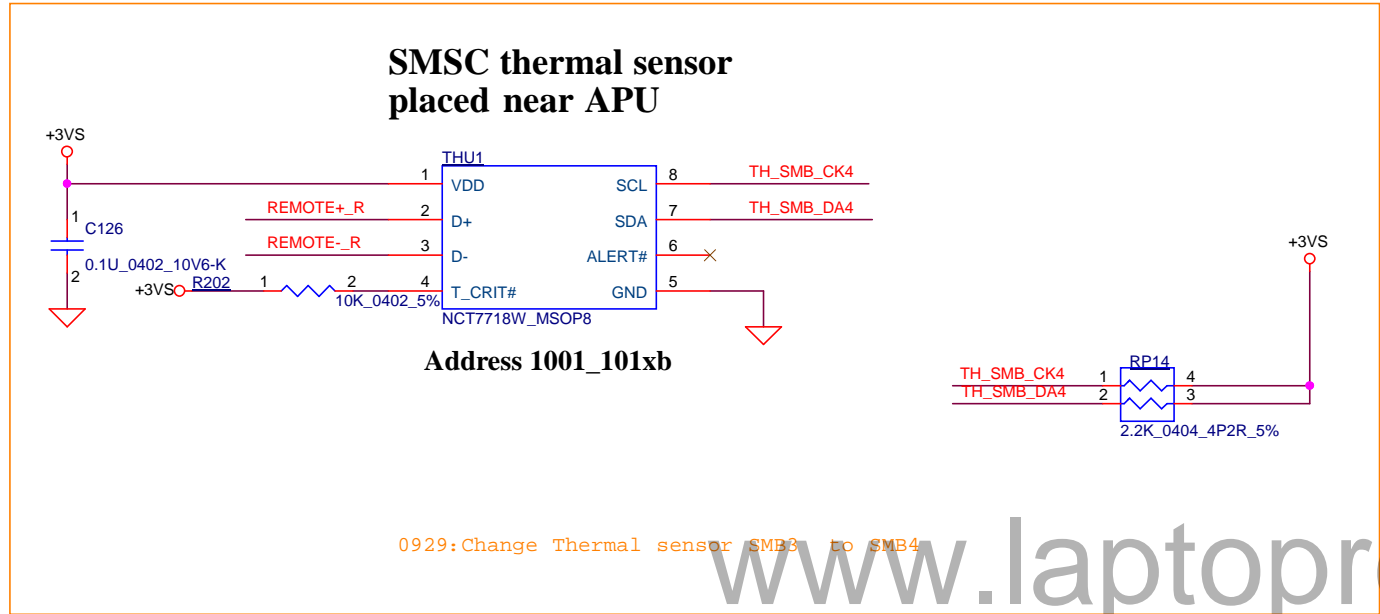
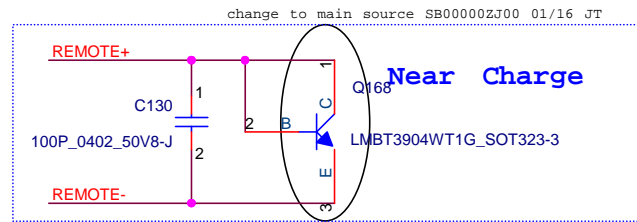



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				JINN/DOOKU	
				Date:	Rev
				Tuesday, April 10, 2018	1.0
				Sheet 48	of 65



REMOTE+/-_R, REMOTE1+/-, REMOTE2+/-:
Trace width/space:10/10 mil
Trace length:<8"



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Issued Date		2017/02/16		Deciphered Date		2018/06/01			
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Size		Document Number				Rev			
Custom		JINN/DOOKU				1.0			
Date:		Tuesday, April 10, 2018		Sheet		49		of 65	

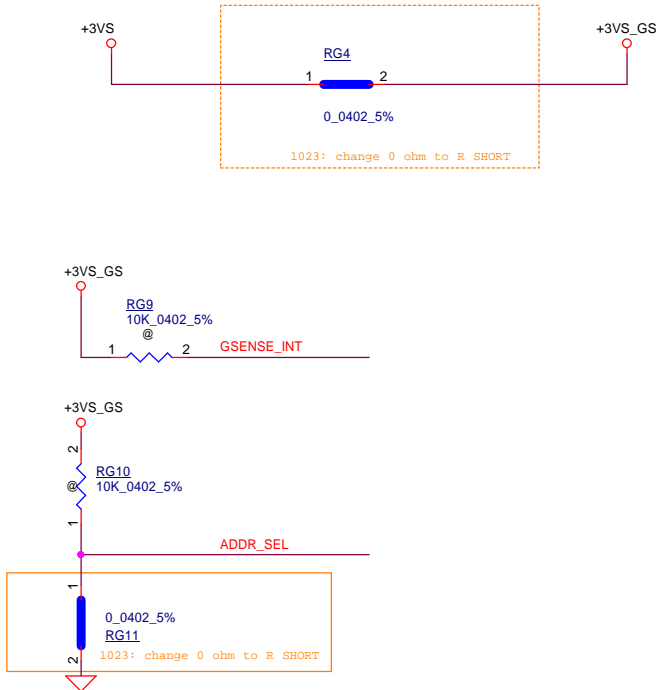
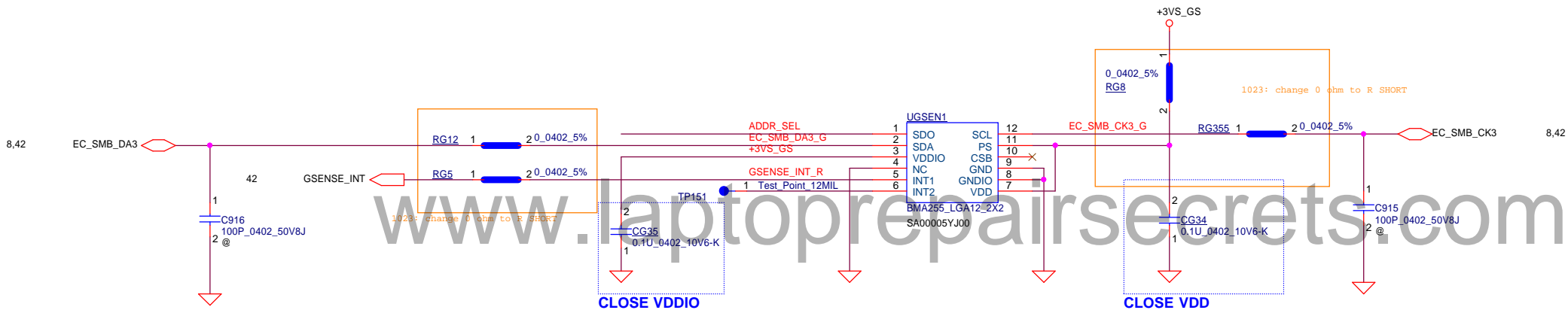


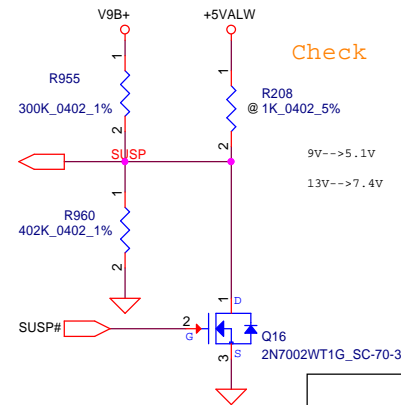
TABLE of G-Sensor (UG5701)		
Vendor	P/N	LCFC P/N
BOSCH	BMA255	SA00005YJ00
Kionix	KX022-1020	SA000081E00

TABLE		
P/N	ADDR_SEL	Address
BMA255	H	32h (W) & 33h (R)
	L	30h (W) & 31h (R)
KX022-1020	H	3Eh (W) & 3Fh (R)
	L	3Ch (W) & 3Dh (R)

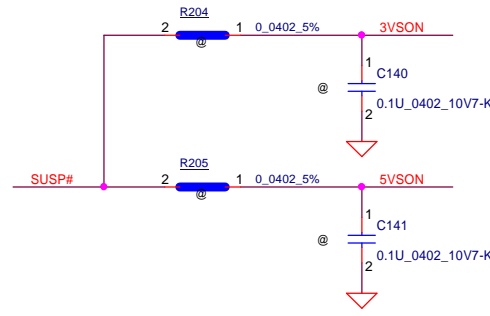
APS G-Sensor



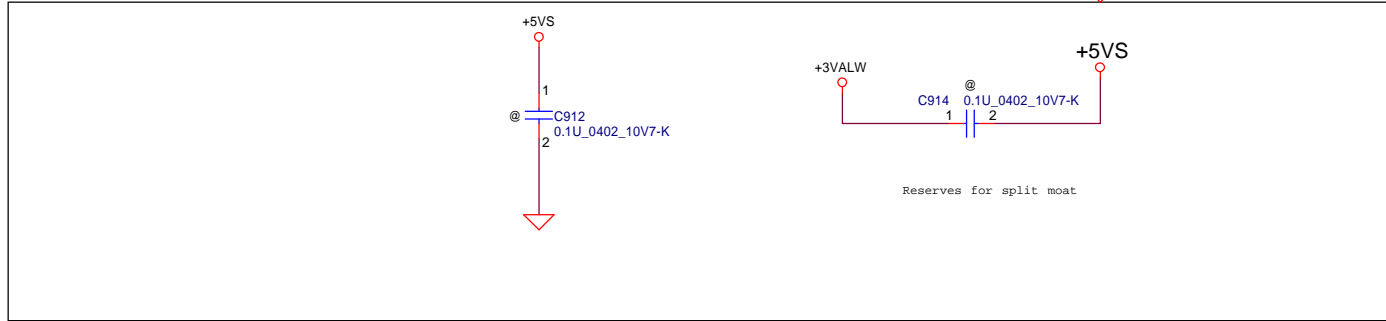
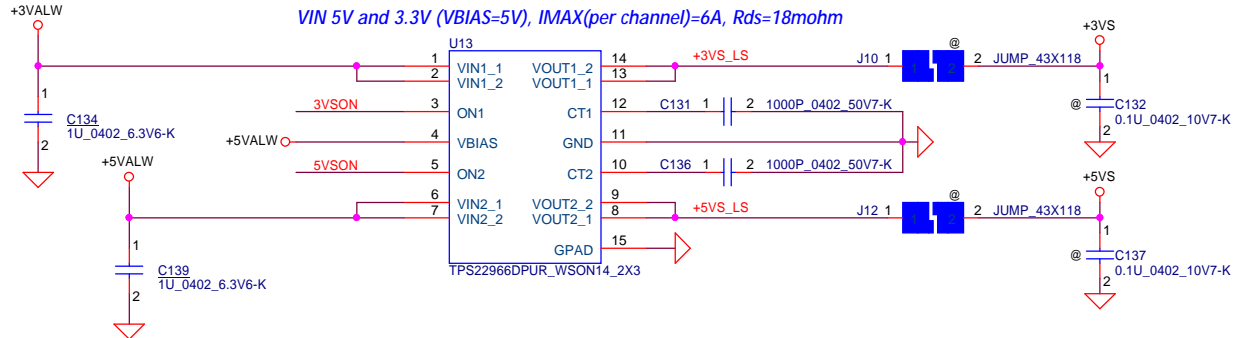
Check 5VLP



Check



3/5VS load Switch
+5VALW To +5VS
+3VALW To +3VS

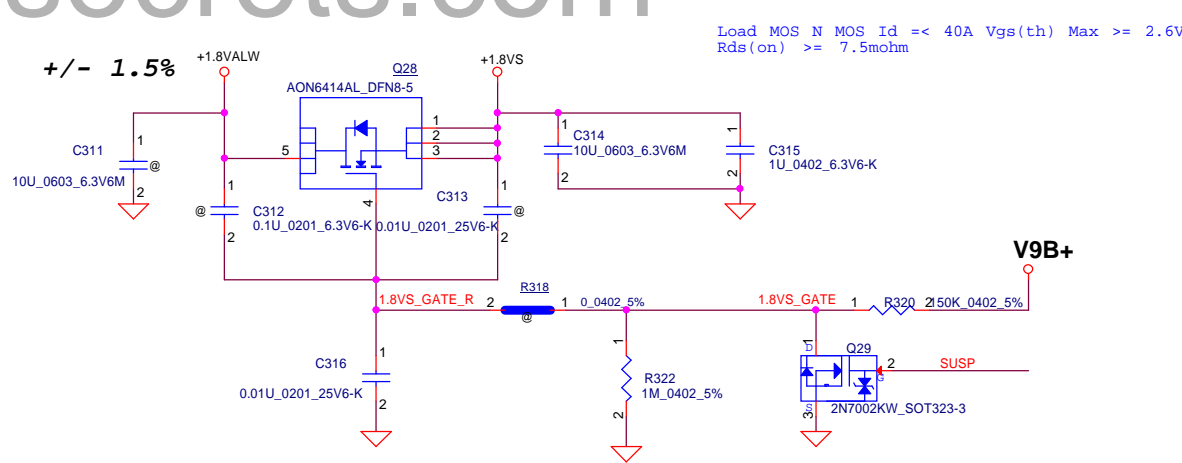
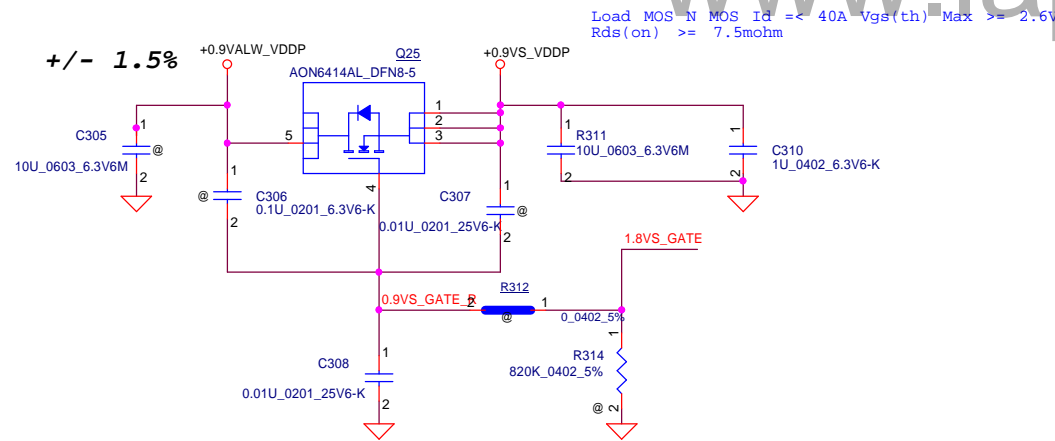


+0.9VALW_VDDP to +VDDP 10A request
V9B+ 9V--13V

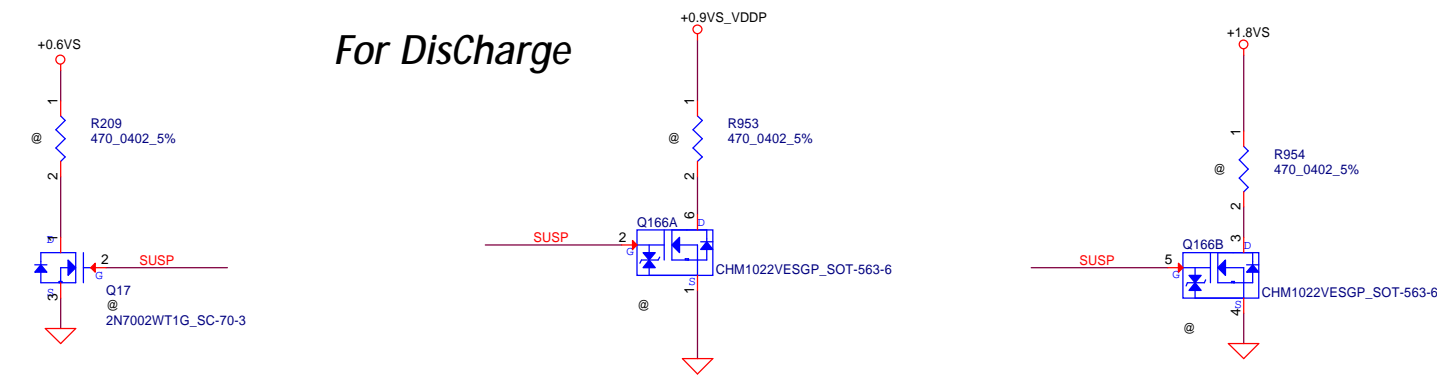
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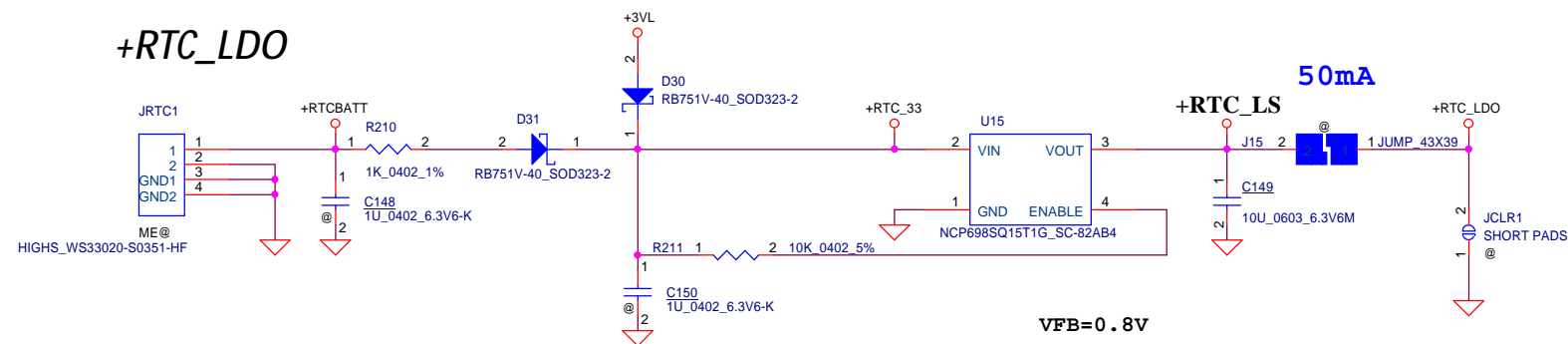
+1.8VALW to +1.8VS 2A request




For DisCharge

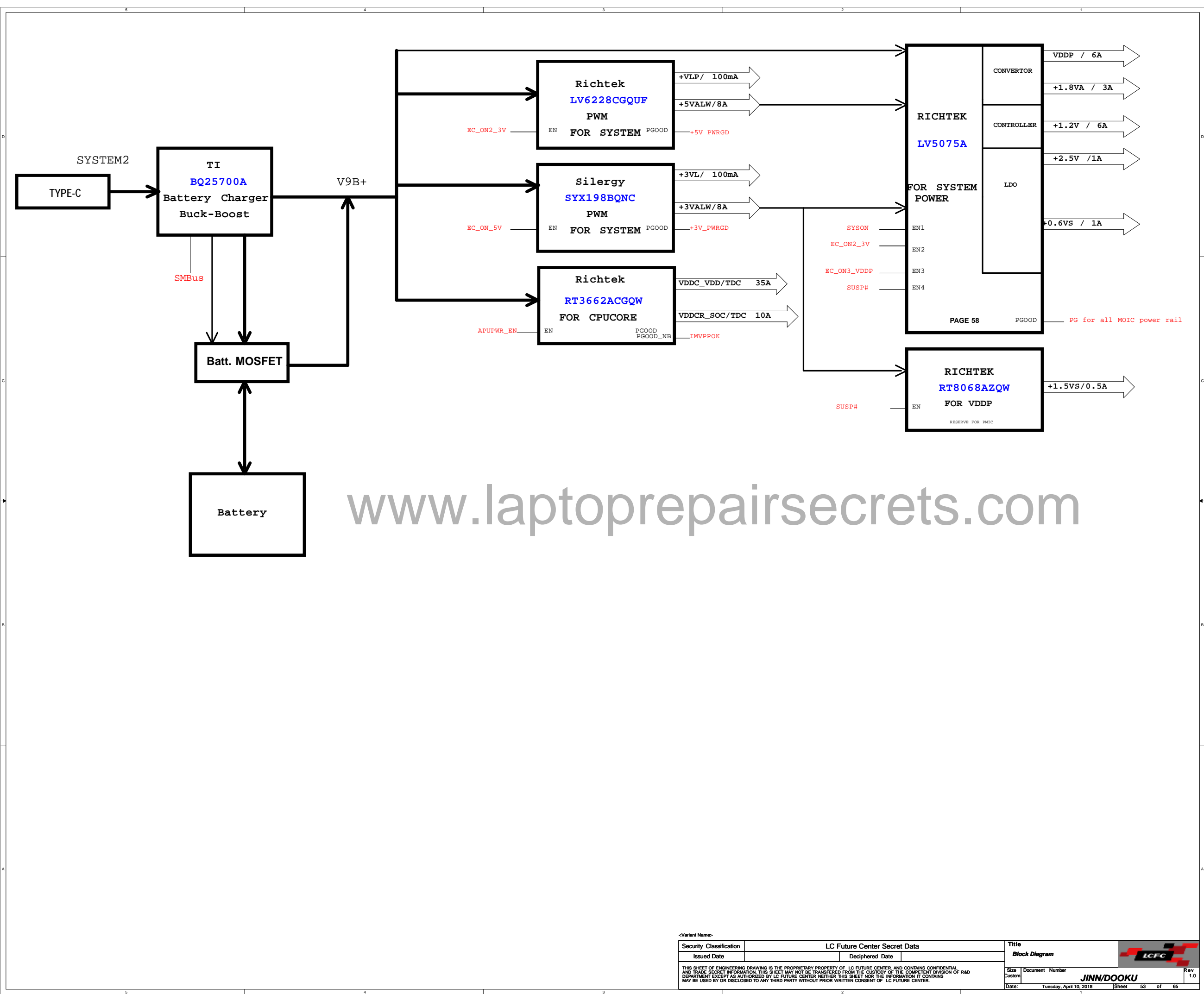


Security Classification	LC Future Center Secret Data			Title	
Issued Date	2017/02/16	Deciphered Date	2018/06/01	3VS/5VS/1.1VS/VDDIO_RUN	
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					Document Number
					JINN/DOOKU
					Rev 1.0
					Date: Tuesday, April 10, 2018
					Sheet 51 of 65

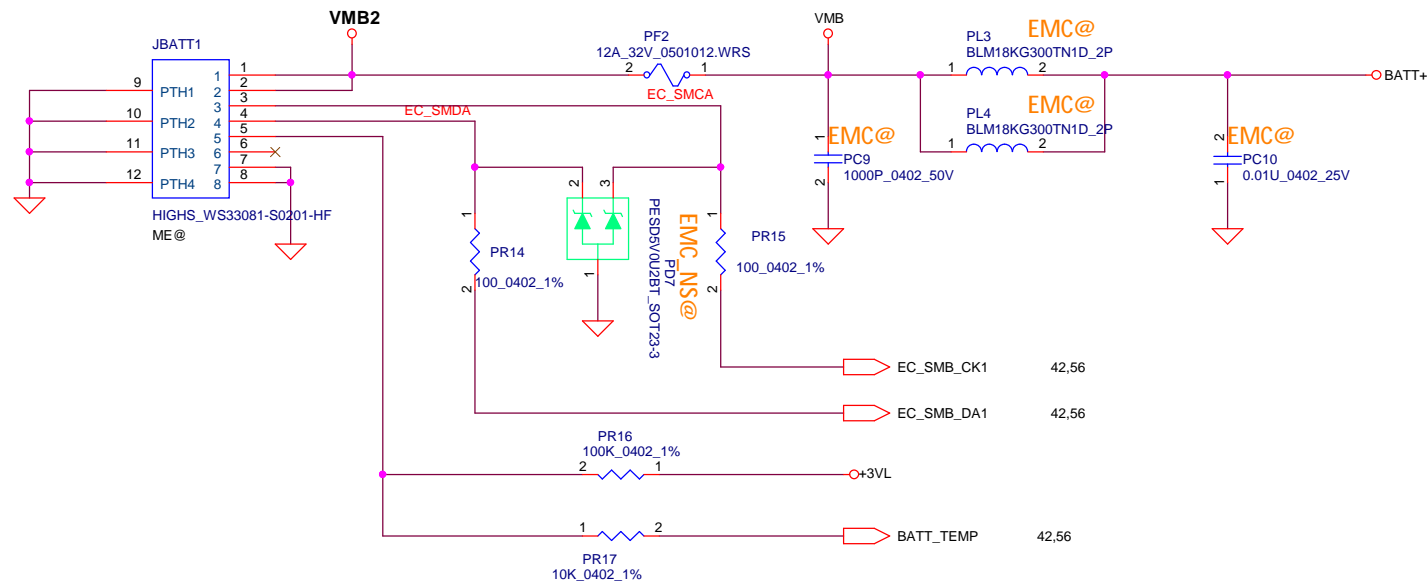


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Security Classification		LC Future Center Secret Data		Title		
Issued Date	2017/02/16	Deciphered Date	2018/06/01	3VS/5VS/1.1VS/VDDIO_RUN		
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				Date: Tuesday, April 10, 2018	Sheet 52 of 65	Rev 1.0



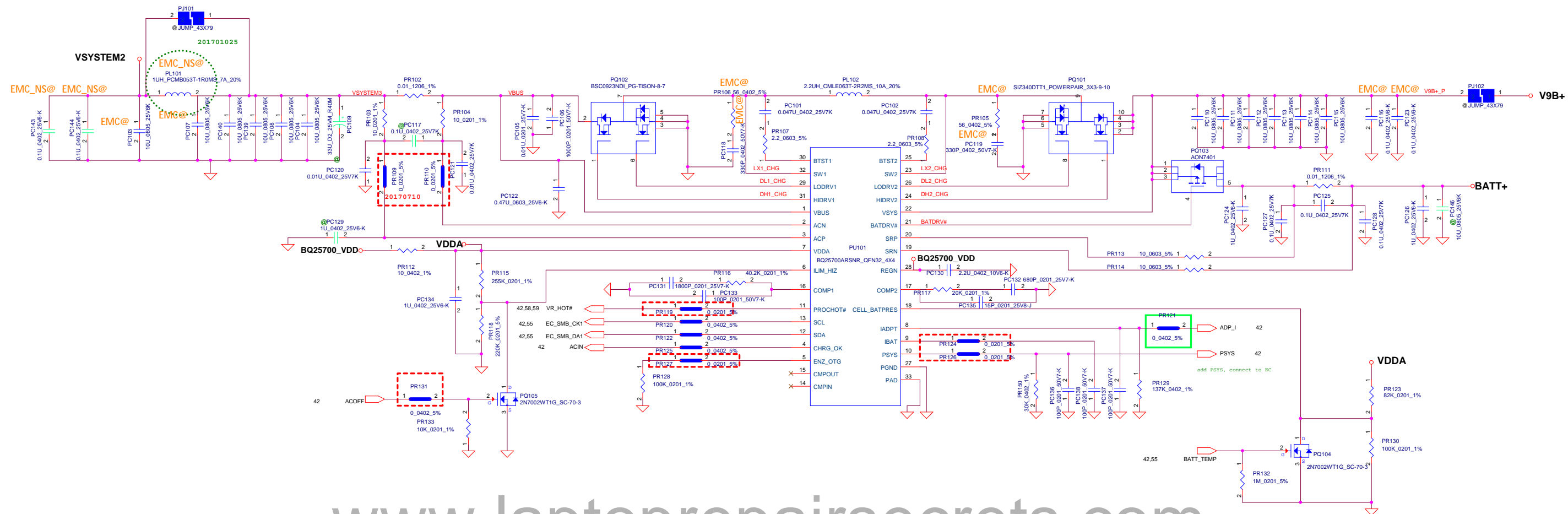




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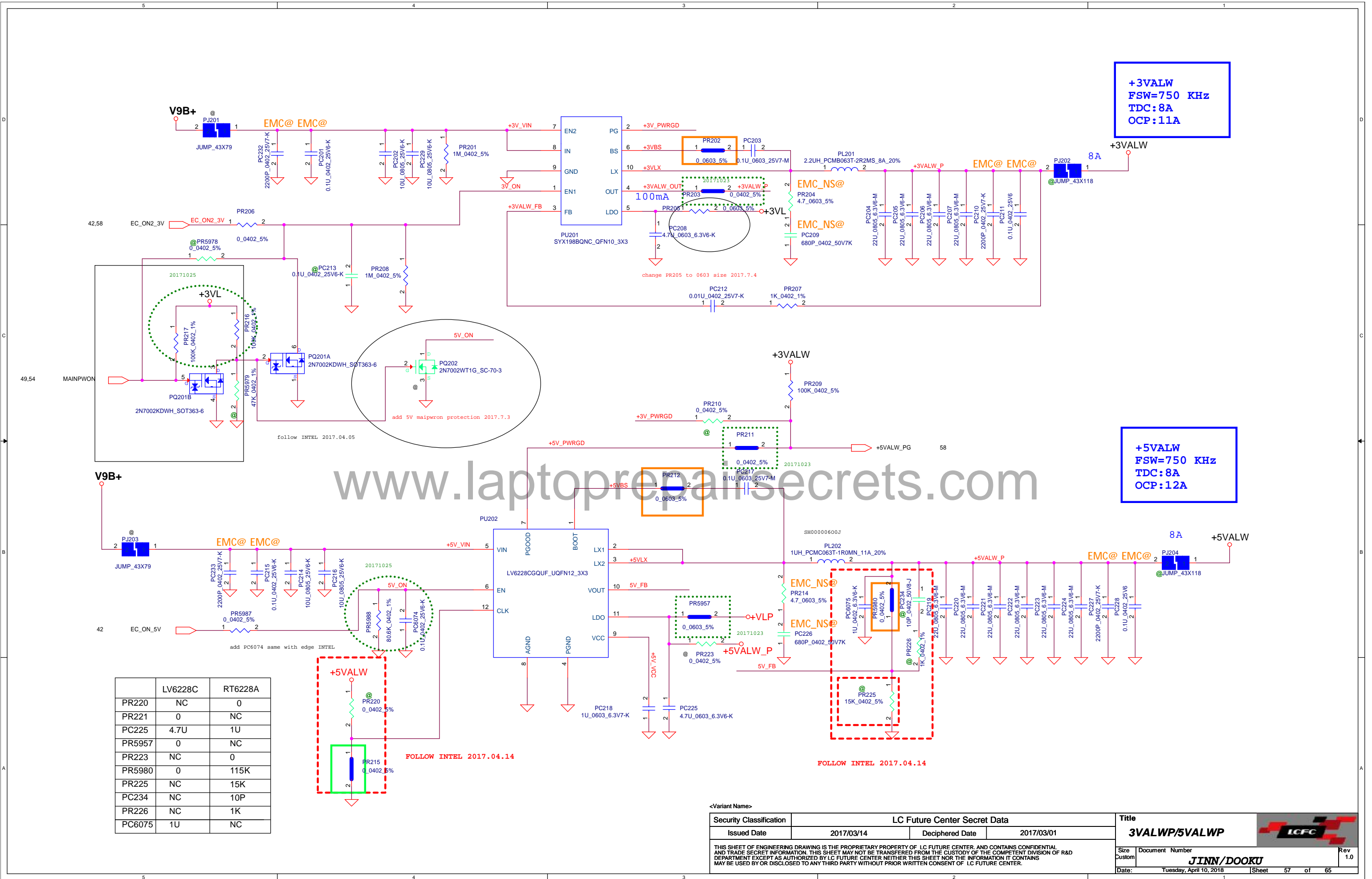
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				Custom	1.0
Date:		Tuesday, April 10, 2018		JINN/DOOKU	
				Sheet	55 of 65



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Issued Date	2013/08/05	Deciphered Date	2014/12/31
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Size	Document	Number	Rev
Custom		EE480 NM-B421	1.0
Date:	Tuesday, April 10, 2018	Sheet	56 of 65



+3VALW
FSW=750 KHz
TDC: 8A
OCP: 11A

+5VALW
FSW=750 KHz
TDC: 8A
OCP: 12A

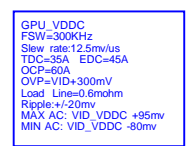
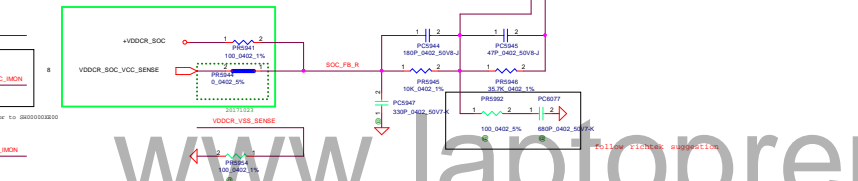
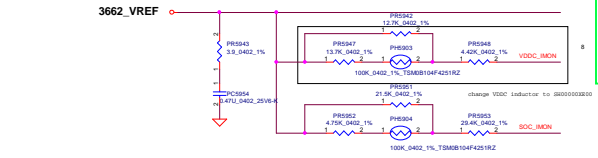
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PR221	0	NC
PC225	4.7U	1U
PR5957	0	NC
PR223	NC	0
PR5980	0	115K
PR225	NC	15K
PC234	NC	10P
PR226	NC	1K
PC6075	1U	NC

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Size Custom	Document Number JINN/DOOKU	Rev 1.0
Date: Tuesday, April 10, 2018		Sheet 57 of 65



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Size		Document Number		JINN/DOOKU		Rev	
Custom						1.0	
Date:		Tuesday, April 10, 2018		Sheet 60 of 65			

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				JINN/DOOKU		Rev 1.0
Date:				Tuesday, April 10, 2018		Sheet 61 of 65

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Security Classification	LC Future Center Secret Data			Title +1.8VGS		
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				Date:	Tuesday, April 10, 2018	Sheet 62 of 65

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Security Classification	LC Future Center Secret Data			Title +1.8VGS		
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				Date:	Tuesday, April 10, 2018	Sheet 62 of 65

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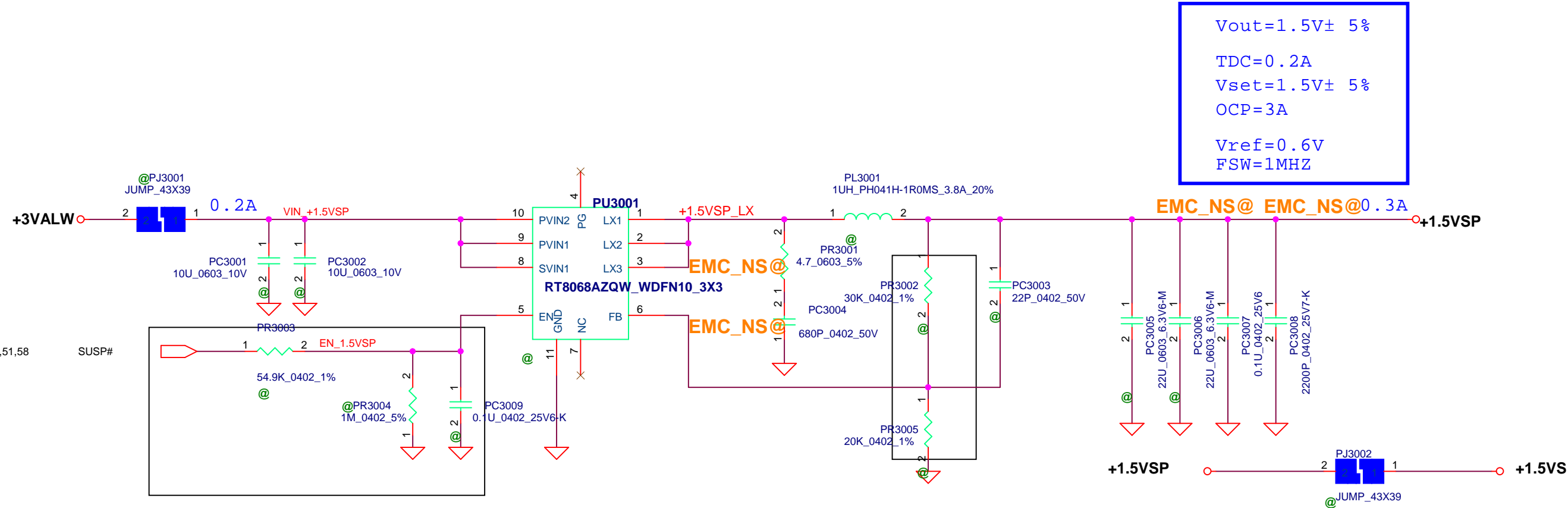
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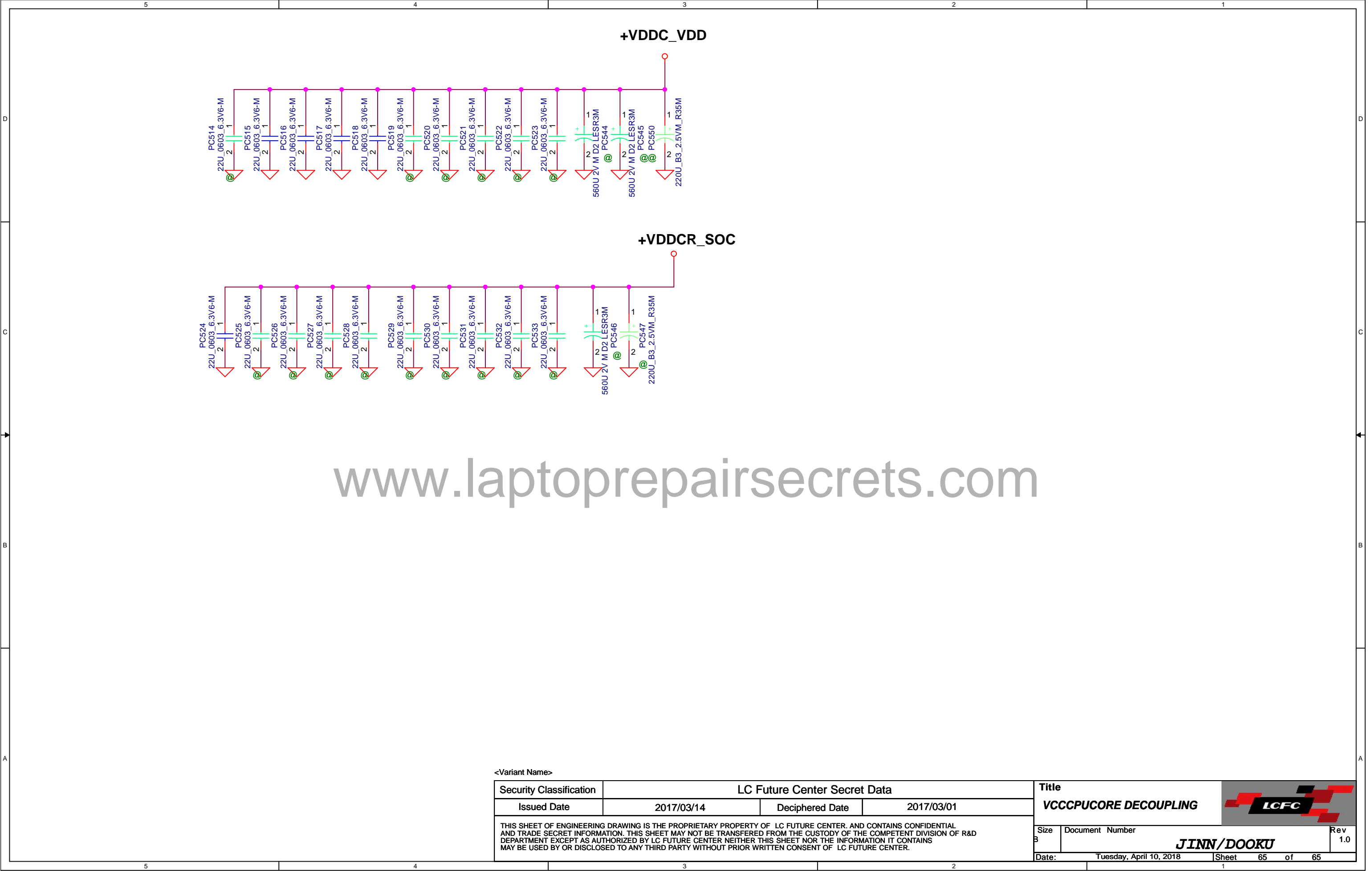
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				Date: Tuesday, April 10, 2018		Sheet 63 of 65	



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Reserve for PMIC

Title		
+1.5VS		
Size	Document Number	Rev
B	<Doc>	1.0
Date:	Tuesday, April 10, 2018	Sheet 64 of 65



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Rev 1.0